

6-ch Charge Pump LED Driver





with Ambient Light Control and Four Integrated LDOs BD6083GUL

General Description

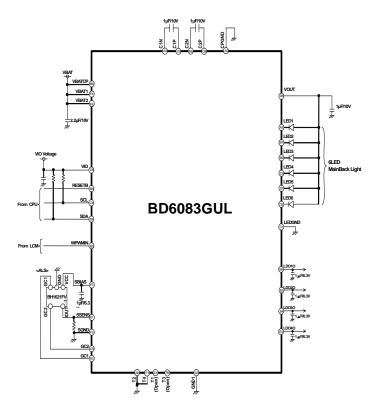
BD6083GUL is "Intelligent LED Driver" that is the most suitable for the cellular phone. It has 3 - 6LED driver and output variable LDO4ch for LCD Backlight.

It has ALC function that is "Low Power Consumption System" realized. It can be developed widely from the high End model to the Low End model. As it has charge pump circuit for DCDC, it is no need to use coils, and it contributes to small space. VCSP50L3 (3.15mm x 3.15mm 0.5mm pitch) It adopts the very thin CSP package that is the most suitable for the slim phone.

Features

- Total 3 6LEDs driver for LCD Backlight
 - It has 4LEDs (it can select 4LED or 3LED) for exclusive use of Main and 2LEDs which can chose independent control or a main allotment by register setting.
 - Main Group" can be controlled by Auto Luminous Control (ALC) system. "Main Group" can be controlled by external PWM signal.
 - ON/ Off and a setup of LED current are possible at the time of the independent control by the independence.

Typical Application Circuit



■ Ambient Light sensor interface

- Incorporates various functions such as a sensor bias adjustment function, an ADC with an average filter, a gain offset adjustment function and an LOG conversion function so that options can be increased for illumination intensity sensors (Photo Diode, Photo Transistor, Photo IC (Linear/LOG)).
- Incorporates an auto gain switching function for suppressing an illumination intensity sensor current at high illumination intensity and improving sensitivity at low illumination intensity
- Capable of customizing an LED current value according to a table setting.
- Slope control loading and an independent control change are possible.
- Charge Pump DC/DC for LED driver
 - It has x1/x1.5/ x2 mode that will be selected automatically.
 - The most suitable voltage up magnification is controlled automatically by LED port voltage.
 - Soft start functions. Over voltage protection (Auto-return type), Over current protection (Auto-return type) loading
- 4ch Series Regulator (LDO)
 - It has selectable output voltage by the register.(16 steps)
- Thermal shutdown
- I²C BUS FS mode (max 400 kHz) Compatibility

●Key Specification

Operating power supply voltage range: 2.7V to 5.5V
 LED maximum setup current: 25.6mA (Typ.)
 Switching frequency: 1.0MHz (Typ.)
 Operating temperature range: -30°C to +85°C

●Package VCSP50L3 W(Typ.) x D(Typ.) x H(Max.) 3.15mm x 3.15mm x 0.55mm

●Pin Configuration [Bottom View]

,UII	ııyuratı	OII LE	ottom	AIGMI		
F	T4	SGND	SBIAS	VBAT1	VBAT2	Т3
E	SSENS	GC1	LD040	LD030	LD020	LD010
D	LED5	LED6	GC2	SDA	VIO	VOUT
С	LEDGND	LED4	index	SCL	C1P	C2P
В	LED2	LED3	WPWMIN	RESETB	C2N	VBATCP
Α	T1	LED1	GND1	C1N	CPGND	Т2
	1 Total 35 Ball	2	3	4	5	6

OProduct structure: Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays

● Absolute Maximum Ratings (Ta=25 °C)

Parameter	Symbol	Ratings	Unit
Maximum Voltage	VMAX	7	V
Power Dissipation	Pd	1280 ^(Note)	mW
Operating Temperature Range	Topr	-30 to +85	°C
Storage Temperature Range	Tstg	-55 to +150	°C

(Note) Power dissipation deleting is 10.24mW/ °C , when it's used in over 25 °C. (It's deleting is on the board that is ROHM's standard)

■Recommended Operating Ratings (VBAT≥VIO, Ta=-30 to 85 °C)

Parameter	Symbol	Limits	Unit
VBAT Input Voltage	VBAT	2.7 to 5.5	V
VIO Pin Voltage	VIO	1.65 to 3.3	V

● Electrical Characteristics (Unless otherwise specified, Ta=25°C, VBAT=3.6V, VIO=1.8V)

Electrical Characteristics (Unles		becilied, 1	Limits	JA1 = 3.0 v,		
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
[Circuit Current]			.,,,,,			
VBAT Circuit Current 1	IBAT1	-	0.1	3.0	μA	RESETB=0V, VIO= 0V
VBAT Circuit Current 2	IBAT2	-	0.5	3.0	μA	RESETB=0V, VIO=1.8V
VBAT Circuit Current 3	IBAT3	-	61	65	mA	DC/DC x1 mode, lo=60mA VBAT=4.0V
VBAT Circuit Current 4	IBAT4	-	92	102	mA	DC/DC x1.5 mode, lo=60mA VBAT=3.6V
VBAT Circuit Current 5	IBAT5	-	123	140	mA	DC/DC x2 mode, Io=60mA VBAT=2.7V
VBAT Circuit Current 6	IBAT6	-	0.25	1.0	mA	ALC Operating ALCEN=1, AD cycle=0.5s setting Except sensor current
VBAT Circuit Current 7	IBAT7	-	90	150	μΑ	LDO1,2=ON, I _{LDO} =0mA
VBAT Circuit Current 8	IBAT8	-	90	150	μΑ	LDO3,4=ON, I _{LDO} =0mA
【LED Driver】						
LED Current Step (Setup)	ILEDSTP1		128		Step	LED1 to 6
LED Current Step (At slope)	ILEDSTP2		256		Step	LED1 to 6
LED Maximum Setup Current	IMAXWLED	-	25.6	-	mΑ	LED1 to 6
LED Current Accuracy	IWLED	-7	-	+7	%	ILED=15mA setting, VLED=1.0V
LED Current Matching	ILEDMT	-	-	4	%	Between LED1 to 6 at VLED=1.0V, ILED=15mA
LED OFF Leak Current	ILKLED	-	-	1.0	μΑ	VLED=4.5V
【DC/DC(Charge Pump)】				,		
Output Voltage	VoCP	-	Vf+0.2	Vf+0.25	V	Vf is forward direction of LED
Drive Ability	IOUT	-	-	150	mΑ	VBAT≥3.2V, VOUT=3.9V
Switching Frequency	fosc	0.8	1.0	1.2	MHz	
Over Voltage Protection Detect Voltage	OVP	-	5.6	-	V	
Over Current Protection Detect Current	ОСР	-	250	375	mA	VOUT=0V
[Sensor Interface]						
SBIAS	VoS	2.85	3.0	3.15	V	Io=200μA
Output Voltage SBIAS Maximum Output Current	IomaxS	30	-	-	mA	
SBIAS Discharge Resistor at OFF	ROFFS	-	1.0	1.5	kΩ	
SSENS Input Range	VISS	0	-	VoS × 255/256	V	
ADC Resolution	ADRES		8		bit	
ADC Integral Calculus Non-linearity	ADINL	-3	-	+3	LSB	
ADC Differential Calculus Non-linearity	ADDNL	-1	-	+1	LSB	

● Electrical Characteristics - continued (Unless otherwise specified, Ta=25°C, VBAT=3.6V, VIO=1.8V)

Parameter	Symbol	NAir	Limits	Mari	Unit	Condition
【Regulator (LDO1)】		Min.	Тур.	Max.		
[Negulator (LDO1)]		1.164	1.20	1.236	V	Io=50mA
		1.261	1.30	1.339	V	Io=50mA
		1.455	1.50	1.545	V	Io=50mA
		1.552	1.60	1.648	V	Io=50mA
		1.746	1.80	1.854	V	Io=50mA <initial voltage=""></initial>
		2.134	2.20	2.266	V	Io=50mA
	Vo1	2.328	2.40	2.472	V	Io=50mA
		2.425	2.50	2.575	V	Io=50mA
Output Voltage		2.522	2.60	2.678	V	Io=50mA
		2.619	2.70	2.781	V	Io=50mA
		2.716	2.80	2.884	V	Io=50mA
		2.813	2.90	2.987	V	Io=50mA
		2.910	3.00	3.090	V	Io=50mA
		3.007	3.10	3.193	V	Io=50mA
		3.104	3.20	3.296	V	Io=50mA
		3.201	3.30	3.399	V	Io=50mA
Output Current	lo1	-	-	150	mA	Vo=1.8V
Dropout Voltage	Vsat1	-	0.2	0.3	V	VBAT=2.5V, Io=150mA, Vo=2.8V
Load Stability	ΔVo11	-	10	60	mV	Io=1 to 150mA, Vo=1.8V
Input Voltage Stability	ΔVo12	-	10	60	mV	VBAT=3.4 to 4.5V, Io=50mA, Vo=1.8
· · · · · · · · · · · · · · · · · · ·						f=100Hz, Vin=200mVp-p, Vo=1.2V
Ripple Rejection Ratio	RR1	-	65	-	dB	Io=50mA, BW=20Hz to 20kHz
Short Circuit Current Limit	llim1	-	200	400	mA	Vo=0V
Discharge Resistor at OFF	ROFF1	-	1.0	1.5	kΩ	
[Regulator (LDO2)]						
		1.164	1.20	1.236	V	Io=50mA
		1.261	1.30	1.339	V	Io=50mA
		1.455	1.50	1.545	V	Io=50mA
		1.552	1.60	1.648	V	Io=50mA
		1.746	1.80	1.854	V	Io=50mA
		2.134	2.20	2.266	V	Io=50mA
		2.328	2.40	2.472	V	Io=50mA
Output Voltage	Vo2	2.425	2.50	2.575	V	Io=50mA <initial voltage=""></initial>
Output voltage	V02	2.522	2.60	2.678	V	Io=50mA
		2.619	2.70	2.781	V	Io=50mA
		2.716	2.80	2.884	V	Io=50mA
		2.813	2.90	2.987	V	Io=50mA
		2.910	3.00	3.090	V	Io=50mA
		3.007	3.10	3.193	V	Io=50mA
		3.104	3.20	3.296	V	Io=50mA
		3.201	3.30	3.399	V	Io=50mA
Output Current	lo2	-	-	150	mA	Vo=2.5V
Dropout Voltage	Vsat2	-	0.2	0.3	V	VBAT=2.5V, Io=150mA, Vo=2.8V
Load Stability	Δvo21	-	10	60	mV	Io=1 to 150mA, Vo=2.5V
Input Voltage Stability	Δνο22	-	10	60	mV	VBAT=3.4 to 4.5V, Io=50mA, Vo=2.5V
Ripple Rejection Ratio	RR2	-	65	-	dB	f=100Hz, Vin=200mVp-p, Vo=1.2V Io=50mA, BW=20Hz to 20kHz
Short Circuit Current Limit	Ilim2	-	200	400	mA	Vo=0V
Discharge Resistor at OFF	ROFF2	_	1.0	1.5	kΩ	

● Electrical Characteristics - continued (Unless otherwise specified, Ta=25°C, VBAT=3.6V, VIO=1.8V)

Parameter [Regulator (LDO3)]	Symbol	B 4:			Unit	Condition	
[Regulator (LDO3)]		Min.	Тур.	Max.	Offic	Condition	
_ 0 \/-							
		1.164	1.20	1.236	V	Io=50mA	
		1.261	1.30	1.339	V	Io=50mA	
		1.455	1.50	1.545	V	Io=50mA	
		1.552	1.60	1.648	V	Io=50mA	
		1.746	1.80	1.854	V	Io=50mA <initial voltage=""></initial>	
		2.134	2.20	2.266	V	Io=50mA	
		2.328	2.40	2.472	V	Io=50mA	
Output Voltage	Vo3	2.425	2.50	2.575	V	Io=50mA	
- Carpar remage		2.522	2.60	2.678	V	Io=50mA	
		2.619	2.70	2.781	V	Io=50mA	
		2.716	2.80	2.884	V	Io=50mA	
		2.813	2.90	2.987	V	Io=50mA	
		2.910	3.00	3.090	V	Io=50mA	
		3.007	3.10	3.193	V	Io=50mA	
		3.104	3.20	3.296	V	Io=50mA	
		3.201	3.30	3.399	V	Io=50mA	
Output Current	lo3	-	-	150	mA	Vo=1.8V	
Dropout Voltage	Vsat3	-	0.2	0.3	V	VBAT=2.5V, Io=150mA, Vo=2.8V	
Load Stability	ΔVo31	-	10	60	mV	Io=1 to 150mA, Vo=1.8V	
Input Voltage Stability	ΔVo32	-	10	60	mV	VBAT=3.4 to 4.5V, Io=50mA, Vo=1.8V	
Ripple Rejection Ratio	RR3	-	65	-	dB	f=100Hz, Vin=200mVp-p, Vo=1.2V Io=50mA, BW=20Hz to 20kHz	
Short Circuit Current Limit	Ilim3	_	200	400	mA	Vo=0V	
Discharge Resistor at OFF	ROFF3	-	1.0	1.5	kΩ	V0-0V	
	KUFF3	-	1.0	1.5	K12		
【Regulator (LDO4)】		4.404	4.00	4.000		1 50 4	
		1.164	1.20	1.236	V	Io=50mA	
		1.261	1.30	1.339	V	Io=50mA	
		1.455	1.50	1.545	V	Io=50mA	
		1.552	1.60	1.648	V	Io=50mA	
		1.746	1.80	1.854	V	lo=50mA	
		2.134	2.20	2.266	V	Io=50mA	
		2.328	2.40	2.472	V	Io=50mA	
Output Voltage	Vo4	2.425	2.50	2.575	V	Io=50mA	
		2.522	2.60	2.678	V	Io=50mA	
		2.619	2.70	2.781	V	Io=50mA	
		2.716	2.80	2.884	V	lo=50mA <initial voltage=""></initial>	
		2.813	2.90	2.987	V	Io=50mA	
		2.910	3.00	3.090	V	Io=50mA	
		3.007	3.10	3.193	V	Io=50mA	
		3.104	3.20	3.296	V	Io=50mA	
0		3.201	3.30	3.399	V	Io=50mA	
Output Current	lo4	-	-	150	mA	Vo=2.8V	
Dropout Voltage	Vsat4	-	0.2	0.3	V	VBAT=2.5V, Io=150mA, Vo=2.8V	
Load Stability	ΔVo41	-	10	60	mV	Io=1 to 150mA, Vo=2.8V	
Input Voltage Stability	ΔVo42	-	10	60	mV	VBAT=3.4 to 4.5V, Io=50mA, Vo=2.8V	
Ripple Rejection Ratio	RR4	-	65	-	dB	f=100Hz, Vin=200mVp-p, Vo=1.2V Io=50mA, BW=20Hz to 20kHz	
Short Circuit Current Limit	Ilim4	-	200	400	mA	Vo=0V	
SHOR CIRCUIT CULTERIL LITTIL		1					

● Electrical Characteristics - continued (Unless otherwise specified, Ta=25°C, VBAT=3.6V, VIO=1.8V)

Priectifical Offaracteristics - co	iiiiiiaca ((01110000011101	wide opcome	7a, 1a-20 0,	<u> </u>	0.01, 110-1.01)			
Parameter	Symbol		Limits		Unit	Condition			
Parameter	Symbol	-	Тур.	Max.					
【SDA, SCL】 (I ² C Interface)									
L Level Input Voltage	VILI	-0.3	-	0.25 ×VIO	V				
H Level Input Voltage	VIHI	0.75 ×VIO	-	VBAT+0.3	V				
Hysteresis of Schmitt trigger Input	Vhysl	0.05 ×VIO	-	-	V				
L Level Output Voltage	VOLI	0	-	0.3	V	SDA Pin, IOL=3 mA			
Input Current linl		-	-	1	μΑ	Input Voltage= 0.1xVIO to 0.9xVIO			
【RESETB】 (CMOS Input Pin)									
L Level Input Voltage	VILR	-0.3	-	0.25 ×VIO	V				
H Level Input Voltage	VIHR	0.75 ×VIO	-	VBAT+0.3	V				
Input Current	linR	-	-	1	μA	Input Voltage = 0.1×VIO to 0.9×VIO			
【WPWMIN】 (NMOS Input Pir	n)								
L Level Input Voltage	VILA	-0.3	-	0.3	V				
H Level Input Voltage	VIHA	1.4	-	VBAT+0.3	V				
Input Current	linA	-	3.6	10	μΑ	Input Voltage = 1.8V			
PWM Input Minimum High Pulse Width		250	-	-	μs	WPWMIN Pin			
【GC1, GC2】 (Sensor Gain C	ontrol CM	IOS Output P	in)						
L Level Output Voltage	VOLS	-	-	0.2	V	IOL=1mA			
H Level Output Voltage	VOHS	VoS-0.2	-	-	V	IOH=1mA			

(Unless otherwise specified, Ta=25 °C, VBAT=3.6V, VIO=1.8V)

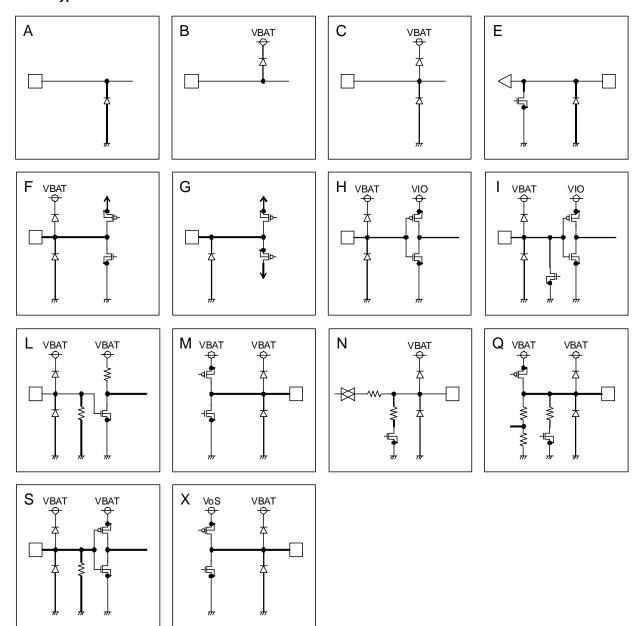
Danamatan	0	Sta	andard-m	ode	F	ast-mod	е	l limit
Parameter	Symbol	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
[I ² C BUS format]		1	1	1	1		1	
SCL clock frequency	fscL	0	-	100	0	-	400	kHz
LOW period of the SCL clock	tLOW	4.7	-	-	1.3	-	-	μs
HIGH period of the SCL clock	tHIGH	4.0	-	-	0.6	-	-	μs
Hold time (repeated) START condition After this period, the first clock is generated	tHD;STA	4.0	-	-	0.6	-	-	μs
Set-up time for a repeated START condition	tsu;sta	4.7	-	-	0.6	-	-	μs
Data hold time	tHD;DAT	0	-	3.45	0	-	0.9	μs
Data set-up time	tsu;dat	250	-	-	100	-	-	ns
Set-up time for STOP condition	tsu;sto	4.0	-	-	0.6	-	-	μs
Bus free time between a STOP and START condition	tBUF	4.7	-	-	1.3	-	-	μs

●Pin Descriptions

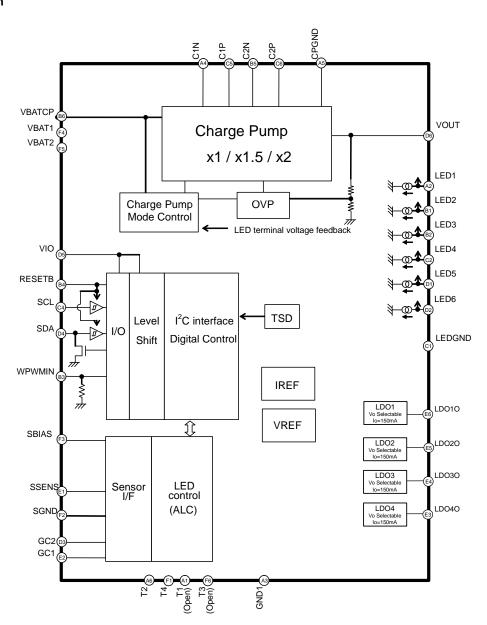
200	J. Iptione						
No	Ball No.	Pin Name	I/O		Diode	Functions	Equivalent Circuit
1	B6	VBATCP	_	For Power	For Ground GND	Pottom is connected	
				-		Battery is connected	A
2	F4	VBAT1	-	-	GND	Battery is connected	A
3	F5	VBAT2	-	-	GND	Battery is connected	A
4	A1	T1	0	VBAT	GND	Test Output Pin(Open)	N
5	A6	T2	I	VBAT	GND	Test Input Pin (short to Ground)	S
6	F6	T3	0	VBAT	GND	Test Output Pin(Open)	M
7	F1	T4	I	VBAT	GND	Test Input Pin (short to Ground)	S
8	D5	VIO	-	VBAT	GND	I/O Power supply is connected	С
9	B4	RESETB	ı	VBAT	GND	Reset input (L: reset, H: reset cancel)	Н
10	D4	SDA	I/O	VBAT	GND	I ² C data input / output	I
11	C4	SCL	I	VBAT	GND	I ² C clock input	Н
12	A5	CPGND	-	VBAT	-	Ground	В
13	A3	GND1	-	VBAT	-	Ground	В
14	C1	LEDGND	-	VBAT	-	Ground	В
15	A4	C1N	I/O	VBAT	GND	Charge Pump capacitor is connected	F
16	C5	C1P	I/O	-	GND	Charge Pump capacitor is connected	G
17	B5	C2N	I/O	VBAT	GND	Charge Pump capacitor is connected	F
18	C6	C2P	I/O	-	GND	Charge Pump capacitor is connected	G
19	D6	VOUT	0	-	GND	Charge Pump output pin	Α
20	A2	LED1	I	-	GND	LED is connected 1 for LCD Back Light	Е
21	B1	LED2	I	-	GND	LED is connected 2 for LCD Back Light	Е
22	B2	LED3	I	-	GND	LED is connected 3 for LCD Back Light	Е
23	C2	LED4	I	-	GND	LED is connected 4 for LCD Back Light	Е
24	D1	LED5	I	-	GND	LED is connected 5 for LCD Back Light	Е
25	D2	LED6	I	-	GND	LED is connected 6 for LCD Back Light	Е
26	F3	SBIAS	0	VBAT	GND	Bias output for the Ambient Light Sensor	Q
27	E1	SSENS	I	VBAT	GND	Ambient Light Sensor input	N
28	E2	GC1	0	VBAT	GND	Ambient Light Sensor gain control output 1	Х
29	D3	GC2	0	VBAT	GND	Ambient Light Sensor gain control output 2	Х
30	F2	SGND	-	VBAT	-	Ground	В
31	В3	WPWMIN	I	VBAT	GND	External PWM input for Back Light *	L
32	E6	LDO10	0	VBAT	GND	LDO1 output pin	Q
33	E5	LDO2O	0	VBAT	GND	LDO2 output pin	Q
34	E4	LDO3O	0	VBAT	GND	LDO3 output pin	Q
35	E3	LDO40	0	VBAT	GND	LDO4 output pin	Q
L						T I	-

^{*} A setup of a register is separately necessary to make it effective.

●Pin ESD Type



●Block Diagram



●I²C BUS Format

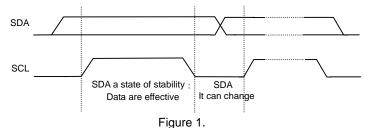
The writing/reading operation is based on the I²C slave standard.

Slave address

A7	A6	A5	A4	A3	A2	A1	R/W
1	1	1	0	1	1	0	1/0

· Bit Transfer

SCL transfers 1-bit data during H. SCL cannot change signal of SDA during H at the time of bit transfer. If SDA changes while SCL is H, START conditions or STOP conditions will occur and it will be interpreted as a control signal.



START and STOP condition

When SDA and SCL are H, data is not transferred on the I²C- bus. This condition indicates, if SDA changes from H to L while SCL has been H, it will become START (S) conditions, and an access start, if SDA changes from L to H while SCL has been H, it will become STOP (P) conditions and an access end.

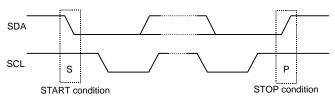


Figure 2.

Acknowledge

It transfers data 8 bits each after the occurrence of START condition. A transmitter opens SDA after transfer 8bits data, and a receiver returns the acknowledge signal by setting SDA to L.

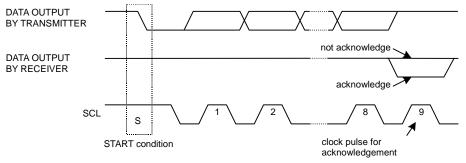


Figure 3.

· Writing protocol

A register address is transferred by the next 1 byte that transferred the slave address and the write-in command. The 3rd byte writes data in the internal register written in by the 2nd byte, and after 4th byte or, the increment of register address is carried out automatically. However, when a register address turns into the last address, it is set to 00h by the next transmission. After the transmission end, the increment of the address is carried out.

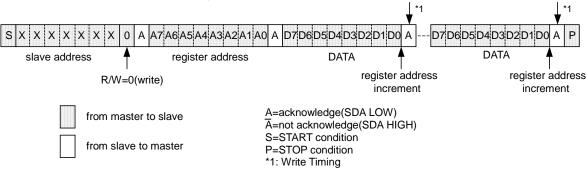


Figure 4.

· Reading protocol

It reads from the next byte after writing a slave address and R/W bit. The register to read considers as the following address accessed at the end, and the data of the address that carried out the increment is read after it. If an address turns into the last address, the next byte will read out 00h. After the transmission end, the increment of the address is carried out.

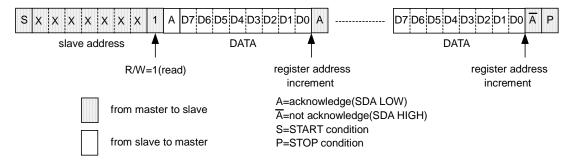


Figure 5.

· Multiple reading protocols

After specifying an internal address, it reads by repeated START condition and changing the data transfer direction. The data of the address that carried out the increment is read after it. If an address turns into the last address, the next byte will read out 00h. After the transmission end, the increment of the address is carried out.

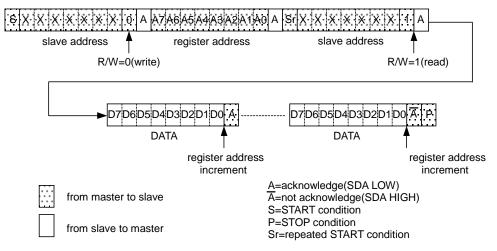


Figure 6.

As for reading protocol and multiple reading protocols, please do \overline{A} (not acknowledge) after doing the final reading operation. It stops with read when ending by A(acknowledge), and SDA stops in the state of Low when the reading data of that time is 0. However, this state returns usually when SCL is moved, data is read, and A (not acknowledge) is done.

●Timing Diagram

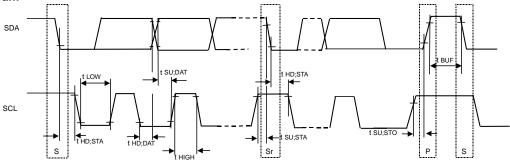


Figure 7.

● Register List

Address	W//D				Regist	er data				Function	
Address	VV/IX	D7	D6	D5	D4	D3	D2	D1	D0	Function	
00h	W	-	-	-	-	-	-	-	SFTRST	Software Reset	
01h	W	-	-	-	-	W6MD	W5MD	W4MD	MLEDMD	LED Pin function setting	
02h	W	WPWMEN	ALCEN	-	-	W6EN	W5EN	-	MLEDEN	LED Power Control	
03h	W	ı	IMLED(6)	IMLED(5)	IMLED(4)	IMLED(3)	IMLED(2)	IMLED(1)	IMLED(0)	Main group current setting	
04h	-	-	-	-	-	-	-	-	-	-	
05h	W	ı	IW5(6)	IW5(5)	IW5(4)	IW5(3)	IW5(2)	IW5(1)	IW5(0)	LED5 current setting	
06h	W	-	IW6(6)	IW6(5)	IW6(4)	IW6(3)	IW6(2)	IW6(1)	IW6(0)	LED6 current setting	
07h			-	-	-	-			-	-	
08h	•	ı	ı	=	ı	=	ı	ı	=	-	
09h	W	THL (3)	THL (2)	THL (1)	THL (0)	TLH (3)	TLH (2)	TLH (1)	TLH (0)	Main Current transition	
0Ah	W	-	ADCYC	-	GAIN	-	-	MDCIR	SBIASON	Measurement mode setting	
0Bh	W	-	-	-	-	CRV	STEP (2)	STEP (1)	STEP (0)	ALC Slope curve setup	
0Ch	R		-	-	-	AMB (3)	AMB (2)	AMB (1)	AMB (0)	Ambient level	
0Dh	W	ı	IU0 (6)	IU0 (5)	IU0 (4)	IU0 (3)	IU0 (2)	IU0 (1)	IU0 (0)	LED Current at Ambient level 0h (ALC)	
0Eh	W	-	IU1 (6)	IU1 (5)	IU1 (4)	IU1 (3)	IU1 (2)	IU1 (1)	IU1 (0)	LED Current max (ALC)	
0Fh	•	ı	ı	=	ı	=	ı	ı	=	-	
10h	,	-	-	-	=	-	-	-	-	-	
11h	,	-	-	-	-	-	-	-	-	-	
12h	-	-	-	-	-	-	-	-	-	-	
13h	W	-	-	-	-	LDO4EN	LDO3EN	LDO2EN	LDO1EN	LDO Power Control	
14h	W	LDO2VSEL3	LDO2VSEL2	LDO2VSEL1	LDO2VSEL0	LDO1VSEL3	LDO1VSEL2	LDO1VSEL1	LDO1VSEL0	LDO1 Vout Control LDO2 Vout Control	
15h	W	LDO4VSEL3	LDO4VSEL2	LDO4VSEL1	LDO4VSEL0	LDO3VSEL3	LDO3VSEL2	LDO3VSEL1	LDO3VSEL0	LDO3 Vout Control LDO4 Vout Control	

Input "0" for "-".
Prohibit to accessing the address that isn't mentioned.

● Register Map

Address 00h < Software Reset >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
00h	W	=	=	=	=	-	-	-	SFTRST
Initial Value	00h	=	-	=	=	-	-	-	0

Bit[7:1]: (Not used)

Bit0: SFTRST Software Reset

"0": Reset cancel

"1": Reset(All register initializing)

Refer to "Reset" for detail.

Address 01h < LED Pin function setting>

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
01h	W	-	-	-	-	W6MD	W5MD	W4MD	MLEDMD
Initial Value	02h	-	-	-	-	0	0	1	0

Bit[7:4]: (Not used)

Bit3: W6MD LED6 control setting (individual / Main group)

"0": LED6 individual control (Initial Value)

"1": LED6 Main group control Refer to "LED Driver" for detail.

Bit2: W5MD LED5 control setting (individual / Main group)

"0": LED5 individual control (Initial Value)

"1": LED5 Main group control Refer to "LED Driver" for detail.

Bit1: W4MD LED4 Control Board setting (unuse / use)

"0": LED4 unuse

"1": LED4 use (Main group Control) (Initial Value)

Refer to "LED Driver" for detail.

Bit0: MLEDMD Main group setting (Normal / ALC)

"0": Main group Normal Mode(ALCNon-reflection)(Initial Value)

"1": Main group ALC Mode

Refer to "(1) Auto Luminous Control ON/OFF" of "ALC" for detail.

Set up a fixation in every design because it isn't presumed W*PW that it is changed dynamically. And, do the setup of W*PW when each LED is Off.

Address 02h < LED Power Control>

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
02h	W	WPWMEN	ALCEN	-	-	W6EN	W5EN	-	MLEDEN
Initial Value	00h	0	0	0	0	0	0	0	0

Bit7: WPWMEN External PWM Input "WPWMIN" terminal Enable Control (Valid/Invalid)

"0": External PWM input invalid (Initial Value)

"1": External PWM input valid

Refer to "(10) Current Adjustment" of "ALC" for detail.

Bit6: ALCEN ALC function Control (ON/OFF)

"0": ALC block OFF (Initial Value)

"1": ALC block ON (Ambient Measurement)

Refer to "(1) Auto Luminous Control ON/OFF" of "ALC" for detail.

Bit[5:4]: (Not used)

Bit3: W6EN LED6 Control (ON/OFF)

"0": LED6 OFF (Initial Value)
"1": LED6 ON(individual control)
Refer to "LED Driver" for detail.

Bit2: W5EN LED5 Control (ON/OFF)

"0": LED5 OFF (Initial Value)
"1": LED5 ON(individual control)
Refer to "LED Driver" for detail.

Bit1: (Not used)

Bit0: MLEDEN Main group LED Control (ON/OFF)

"0": Main group OFF (Initial Value)

"1": Main group ON

Refer to "(1) Auto Luminous Control ON/OFF" of "ALC" for detail.

Address 03h < Main group LED Current setting(Normal Mode) >

		9							
Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
03h	W	-	IMLED(6)	IMLED(5)	IMLED(4)	IMLED(3)	IMLED(2)	IMLED(1)	IMLED(0)
Initial Value	00h	-	0	0	0	0	0	0	0

Bit7: (Not used)

Bit[6:0]: IMLED (6:0) Main Group LED Current Setting at non-ALC mode

6:0)				Setting	at no	n-ALC mo
	0000":		(Initial Value)	"10000	00":	13.0 mA
	0001":	0.4 mA		"10000	01":	13.2 mA
	0010":	0.6 mA		"10000	10":	13.4 mA
	0011" :	0.8 mA		"10000	11":	13.6 mA
	0100" :	1.0 mA		"10001	00" :	13.8 mA
"000	0101" :	1.2 mA		"10001	01" :	14.0 mA
"000	0110" :	1.4 mA		"10001	10" :	14.2 mA
	0111" :	1.6 mA		"10001	11" :	14.4 mA
"000	1000":	1.8 mA		"10010	00" :	14.6 mA
"000	1001":	2.0 mA		"10010	01" :	14.8 mA
"000	1010":	2.2 mA		"10010	10":	15.0 mA
	1011" :	2.4 mA		"10010	11":	15.2 mA
	1100" :	2.6 mA		"10011	00" :	15.4 mA
"000	1101":	2.8 mA		"10011	01":	15.6 mA
	1110" :	3.0 mA		"10011 [·]	10" :	15.8 mA
	1111":	3.2 mA		"10011	11":	16.0 mA
	0000":	3.4 mA		"10100	00" :	16.2 mA
	0001":	3.6 mA		"10100	01":	16.4 mA
	0010":	3.8 mA		"10100	10":	16.6 mA
	0011" :	4.0 mA		"10100	11":	16.8 mA
	0100" :	4.2 mA		"10101	00":	17.0 mA
	0101" :	4.4 mA		"10101	01":	17.2 mA
	0110" :	4.6 mA		"10101	10"	17.4 mA
"001	0111" :	4.8 mA		"10101	11"	17.6 mA
"001	1000" :	5.0 mA		"10110	 00" ·	17.8 mA
	1000 .	5.2 mA		"10110	00 . 01" ·	18.0 mA
	1010" :	5.4 mA		"10110	10" ·	18.2 mA
	1010 .	5.4 mA		"10110	10 . 11" ·	18.4 mA
	1100" :	5.8 mA		"101110	11 . 00" :	18.6 mA
	1100 .	6.0 mA		"101110	00 .	18.8 mA
	1110":	6.2 mA		"10111	∪ i . 1∩" ·	19.0 mA
	1110 .	6.4 mA		"10111 ₂	10 .	19.2 mA
	0000":	6.6 mA		"11000	Π. ΩΩ"·	
	0000 .	6.8 mA		"11000	00 .	19.4 mA 19.6 mA
				"11000	υι. 40".	19.8 mA
	0010":	7.0 mA		"11000	10 .	
	0011":	7.2 mA		"11000	00"	20.0 mA
	0100":	7.4 mA		"11001	00 .	20.2 mA
	0101":	7.6 mA		11001	10"	20.4 mA
	0110":	7.8 mA		"11001 "11001	10 .	20.6 mA
	0111":	8.0 mA		11001	00".	20.8 mA
	1000":	8.2 mA		"11010	00 :	21.0 mA
	1001":	8.4 mA		"11010	UT :	21.2 mA
	1010":	8.6 mA		"11010	10 :	21.4 mA
	1011":	8.8 mA		"11010	11":	21.6 mA
	1100" :	9.0 mA		"110110	00":	21.8 mA
	1101":	9.2 mA		"110110	UT :	22.0 mA
	1110":	9.4 mA		"110111	10":	22.2 mA
	1111":	9.6 mA		"110111	11″:	22.4 mA
	0000":	9.8 mA		"111000	00" :	22.6 mA
	0001":	10.0 mA		"111000	01":	22.8 mA
	0010":	10.2 mA		"11100 ²	10":	23.0 mA
	0011":	10.4 mA		"11100	11":	23.2 mA
	0100":	10.6 mA		"111010	00" :	23.4 mA
	0101":	10.8 mA		"111010	01":	23.6 mA
"011	0110" :	11.0 mA		"11101	10":	23.8 mA
"011	0111" :	11.2 mA		"11101	11" :	24.0 mA
	1000" :	11.4 mA		"111100	00" :	24.2 mA
	1001" :	11.6 mA		"111100	01":	24.4 mA
"011	1010":	11.8 mA		"11110	10":	24.6 mA
	1011" :	12.0 mA		"11110′		24.8 mA
	1100" :	12.2 mA		"111110		25.0 mA
	1101" :	12.4 mA		"111110		25.2 mA
	1110" :	12.6 mA		"111111		25.4 mA
"011	1111" :	12.8 mA		"111111	1":	25.6 mA

Address 05h < LED5 Current setting(Independence control) >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
05h	W	-	IW5(6)	IW5(5)	IW5(4)	IW5(3)	IW5(2)	IW5(1)	IW5(0)
Initial Value	00h	-	0	0	0	0	0	0	0

Bit7: (Not used)

Bit[6:0]: IW5 (6:0) LED5 Current setting

)	LED5 Cur	rent setting		
	"0000000":	0.2 mA (Initial Value)	"1000000":	13.0 mA
	"0000001":	0.4 mA	"1000001":	13.2 mA
	"0000010":	0.6 mA	"1000010":	13.4 mA
	"0000011":	0.8 mA	"1000011":	13.6 mA
	"0000100":	1.0 mA	"1000100":	13.8 mA
	"0000101":	1.2 mA	"1000101":	14.0 mA
	"0000110":	1.4 mA	"1000110":	14.2 mA
	"0000111":	1.6 mA	"1000111":	14.4 mA
	"0001000":	1.8 mA	"1001000":	14.6 mA
	"0001001":	2.0 mA	"1001001":	14.8 mA
	"0001010":	2.2 mA	"1001010":	15.0 mA
	"0001011":	2.4 mA	"1001011":	15.2 mA
	"0001100":	2.6 mA	"1001100":	15.4 mA
	"0001101":	2.8 mA	"1001101":	15.6 mA
	"0001110":	3.0 mA	"1001110":	15.8 mA
	"0001111":	3.2 mA	"1001111":	16.0 mA
	"0010000":	3.4 mA	"1010000":	16.2 mA
	"0010001":	3.6 mA	"1010001":	16.4 mA
	"0010010":	3.8 mA	"1010010":	16.6 mA
	"0010011":	4.0 mA	"1010011":	16.8 mA
	"0010100":	4.2 mA	"1010100":	17.0 mA
	"0010101":	4.4 mA	"1010101":	17.2 mA
	"0010110" : "0010111" :	4.6 mA 4.8 mA	"1010110": "1010111":	17.4 mA
	"0010111 . "0011000" :	5.0 mA	"10111000":	17.6 mA 17.8 mA
	"0011000":	5.2 mA	"1011000":	18.0 mA
	"0011001":	5.4 mA	"1011001":	18.2 mA
	"0011010":	5.6 mA	"1011010":	18.4 mA
	"0011100":	5.8 mA	"1011100":	18.6 mA
	"0011101":	6.0 mA	"1011101":	18.8 mA
	"0011110":	6.2 mA	"1011110" :	19.0 mA
	"0011111":	6.4 mA	"1011111":	19.2 mA
	"0100000":	6.6 mA	"1100000":	19.4 mA
	"0100001":	6.8 mA	"1100001":	19.6 mA
	"0100010":	7.0 mA	"1100010":	19.8 mA
	"0100011" :	7.2 mA	"1100011":	20.0 mA
	"0100100":	7.4 mA	"1100100":	20.2 mA
	"0100101":	7.6 mA	"1100101":	20.4 mA
	"0100110":	7.8 mA	"1100110":	20.6 mA
	"0100111":	8.0 mA	"1100111":	20.8 mA
	"0101000":	8.2 mA	"1101000":	21.0 mA
	"0101001":	8.4 mA	"1101001":	21.2 mA
	"0101010":	8.6 mA	"1101010":	21.4 mA
	"0101011" : "0101100" :	8.8 mA	"1101011" : "1101100" :	21.6 mA
	"0101100":	9.0 mA 9.2 mA	"1101100":	21.8 mA 22.0 mA
	"0101101":	9.4 mA	"1101101":	22.0 mA
	"0101110":	9.6 mA	"1101110":	22.4 mA
	"0110000":	9.8 mA	"1110000":	22.4 mA
	"0110000":	10.0 mA	"1110000":	22.8 mA
	"0110010":	10.2 mA	"1110010":	23.0 mA
	"0110011" :	10.4 mA	"1110011" :	23.2 mA
	"0110100":	10.6 mA	"1110100":	23.4 mA
	"0110101":	10.8 mA	"1110101":	23.6 mA
	"0110110":	11.0 mA	"1110110":	23.8 mA
	"0110111":	11.2 mA	"1110111":	24.0 mA
	"0111000":	11.4 mA	"1111000":	24.2 mA
	"0111001":	11.6 mA	"1111001":	24.4 mA
	"0111010":	11.8 mA	"1111010":	24.6 mA
	"0111011":	12.0 mA	"1111011":	24.8 mA
	"0111100":	12.2 mA	"1111100":	25.0 mA
	"0111101":	12.4 mA	"1111101":	25.2 mA
	"0111110" : "0111111" :	12.6 mA	"1111110" :	25.4 mA
	OTITITE:	12.8 mA	"1111111":	25.6 mA

Address 06h < LED6 Current setting(Independence control) >

			3(1111)						
Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
06h	W	-	IW6(6)	IW6(5)	IW6(4)	IW6(3)	IW6(2)	IW6(1)	IW6(0)
Initial Valu	e 00h	-	0	0	0	0	0	0	0

Bit7: (Not used)

Bit[6:0]: IW6 (6:0) LED6 Current setting

)	LED6 Cur	rent setting		
	"0000000":	0.2 mA (Initial Value)	"1000000":	13.0 mA
	"0000001":	0.4 mA	"1000001":	13.2 mA
	"0000010":	0.6 mA	"1000010":	13.4 mA
	"0000011":	0.8 mA	"1000011":	13.6 mA
	"0000100" :	1.0 mA	"1000100":	13.8 mA
	"0000101" :	1.2 mA	"1000101":	14.0 mA
	"0000110" :	1.4 mA	"1000110":	14.2 mA
	"0000111":	1.6 mA	"1000111":	14.4 mA
	"0001000" : "0001001" :	1.8 mA 2.0 mA	"1001000" : "1001001" :	14.6 mA 14.8 mA
	"0001001":	2.0 MA	"1001001":	15.0 mA
	"0001010":	2.4 mA	"1001010":	15.0 mA
	"0001011":	2.6 mA	"1001100":	15.4 mA
	"0001101" :	2.8 mA	"1001101":	15.6 mA
	"0001110":	3.0 mA	"1001110":	15.8 mA
	"0001111":	3.2 mA	"1001111":	16.0 mA
	"0010000" :	3.4 mA	"1010000":	16.2 mA
	"0010001" :	3.6 mA	"1010001":	16.4 mA
	"0010010":	3.8 mA	"1010010":	16.6 mA
	"0010011" :	4.0 mA	"1010011":	16.8 mA
	"0010100":	4.2 mA	"1010100":	17.0 mA
	"0010101" :	4.4 mA	"1010101":	17.2 mA
	"0010110":	4.6 mA	"1010110":	17.4 mA
	"0010111":	4.8 mA	"1010111":	17.6 mA
	"0011000":	5.0 mA	"1011000": "1011001":	17.8 mA
	"0011001" : "0011010" :	5.2 mA 5.4 mA	"1011001 : "1011010" :	18.0 mA 18.2 mA
	"0011010 . "0011011" :	5.4 MA	"1011010":	18.4 mA
	"0011011":	5.8 mA	"1011101":	18.6 mA
	"0011100":	6.0 mA	"1011101":	18.8 mA
	"0011110" :	6.2 mA	"1011110":	19.0 mA
	"0011111":	6.4 mA	"1011111":	19.2 mA
	"0100000":	6.6 mA	"1100000":	19.4 mA
	"0100001" :	6.8 mA	"1100001":	19.6 mA
	"0100010":	7.0 mA	"1100010":	19.8 mA
	"0100011" :	7.2 mA	"1100011":	20.0 mA
	"0100100" :	7.4 mA	"1100100":	20.2 mA
	"0100101":	7.6 mA	"1100101":	20.4 mA
	"0100110" :	7.8 mA	"1100110" : "1100111" :	20.6 mA
	"0100111" : "0101000" :	8.0 mA 8.2 mA	"1101000":	20.8 mA 21.0 mA
	"0101000 . "0101001" :	8.4 mA	"1101000 .	21.0 mA
	"0101010":	8.6 mA	"1101010":	21.4 mA
	"0101011":	8.8 mA	"1101011":	21.6 mA
	"0101100" :	9.0 mA	"1101100":	21.8 mA
	"0101101" :	9.2 mA	"1101101":	22.0 mA
	"0101110" :	9.4 mA	"1101110":	22.2 mA
	"0101111" :	9.6 mA	"1101111":	22.4 mA
	"0110000":	9.8 mA	"1110000":	22.6 mA
	"0110001" :	10.0 mA	"1110001":	22.8 mA
	"0110010":	10.2 mA	"1110010":	23.0 mA
	"0110011":	10.4 mA 10.6 mA	"1110011": "1110100":	23.2 mA
	"0110100" : "0110101" :	10.8 mA	"1110100":	23.4 mA 23.6 mA
	"0110101":	11.0 mA	"1110110":	23.8 mA
	"0110111":	11.2 mA	"1110111":	24.0 mA
	"0111000" :	11.4 mA	"1111000" :	24.2 mA
	"0111001" :	11.6 mA	"1111001" :	24.4 mA
	"0111010":	11.8 mA	"1111010":	24.6 mA
	"0111011" :	12.0 mA	"1111011":	24.8 mA
	"0111100":	12.2 mA	"1111100":	25.0 mA
	"0111101":	12.4 mA	"1111101":	25.2 mA
	"0111110" :	12.6 mA	"1111110":	25.4 mA
	"0111111" :	12.8 mA	"1111111":	25.6 mA

Address 09h < Main Current slope time setting >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
09h	W	THL(3)	THL(2)	THL(1)	THL(0)	TLH(3)	TLH(2)	TLH(1)	TLH(0)
Initial Value	C7h	1	1	0	0	0	1	1	1

Bit[7:4]: **THL (3:0)** Main LED current Down transition per 0.2mA step

"0000": 0.256 ms "0001": 0.512 ms "0010": 1.024 ms "0011": 2.048 ms "0100": 4.096 ms "0101": 8.192 ms "0110": 16.38 ms "0111": 32.77 ms "1000": 65.54 ms "1001": 131.1 ms "1010": 196.6 ms "1011": 262.1 ms

"1100": 327.7 ms (Initial Value)

"1101": 393.2 ms "1110": 458.8 ms "1111": 524.3 ms

Setting time is counted based on the switching frequency of Charge Pump.

The above value becomes the value of the Typ (1MHz) time.

Refer to page 34 "(8) Slope Process" of "ALC" for detail.

Bit[3:0]: **TLH (3:0)** Main LED current up transition per 0.2mA step

"0000": 0.256 ms "0001": 0.512 ms "0010": 1.024 ms "0011": 2.048 ms "0100": 4.096 ms "0101": 8.192 ms "0110" : "0111" : 16.38 ms

32.77 ms (Initial Value)

"1000": 65.54 ms "1001": 131.1 ms "1010": 196.6 ms "1011": 262.1 ms "1100": 327.7 ms "1101": 393.2 ms "1110": 458.8 ms "1111": 524.3 ms

Setting time is counted based on the switching frequency of Charge Pump.

The above value becomes the value of the Typ (1MHz) time.

Refer to page 34 "(8) Slope Process" of "ALC" for detail.

Address 0Ah < ALC mode setting >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0Ah	W	-	ADCYC	-	GAIN	-	-	MDCIR	SBIASON
Initial Value	01h	-	0	-	0	-	-	0	1

Bit7: (Not used)

Bit6: ADCYC ADC Measurement Cycle

"0": 0.52 s (Initial Value)

"1": 1.05 s

Refer to "(4) A/D conversion" of "ALC" for detail.

Bit5: (Not used)

Bit4: GAIN Sensor Gain Switching Function Control

"0": Auto Change (Initial Value)

"1": Fixed

Refer to "(3) Gain control" of "ALC" for detail.

Bit[3:2]: (Not used)

Bit1: MDCIR LED Current Reset Select by Mode Change

"0": LED current non-reset when mode change (Initial Value)

"1": LED current reset when mode change

Refer to "(9) LED current reset when mode change" of "ALC" for detail.

Bit0: SBIASON

"0": Measurement cycle synchronous
"1": Usually ON (at ALCEN=1) (Initial Value)
Refer to "(4) A/D conversion" of "ALC" for detail.

Address 0Bh < ALC slope curve setting >

	Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
	0Bh	W	-	-	-	-	CRV	STEP (2)	STEP (1)	STEP (0)
İ	Initial Value	00h	-	-	-	-	0	0	0	1

Bit[7:4]: (Not used)

Bit3: CRV Brightness Current Conversion Curve Type

"0" Log curve (Initial Value)

"1" linear

Bit[2:0]: STEP (2:0) Step At the time of Brightness Current Conversion

"000": 1.0mA

"001": 1.1mA (Initial Value)

"010": 1.2mA "011": 1.3mA "100": 1.6mA "101": 1.7mA "110": 1.8mA "111": 1.9mA

Refer to "(7) Convert LED Current" of "ALC" for detail.

Address 0Ch < Ambient level (Read Only) >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0Ch	R	-	-	-	-	AMB(3)	AMB(2)	AMB(1)	AMB(0)
Initial Value	(00h)	-	-	-	-	(0)	(0)	(0)	(0)

Bit[7:4]: (Not used)

"1111":

Ambient Level

Bit[3:0] : **AMB (3:0)** "0000" : "0001" : 0h (Initial Value) 1h "0010": 2h "0011": 3h "0100": 4h "0101": 5h "0110": 6h "0111": 7h "1000": 8h "1001": 9h "1010": Ah "1011": Bh "1100": Ch "1101" : Dh "1110": Eh

Fh

It begins to read Ambient data through I²C, and possible. To the first AD measurement completion, it is AMB(3:0)=0000. Refer to "(6) Ambient level detection" of "ALC" for detail.

Address 0Dh < Ambient LED Current setting >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0Dh	W	-	IU0 (6)	IU0 (5)	IU0 (4)	IU0 (3)	IU0 (2)	IU0 (1)	IU0 (0)
Initial Value	13	-	0	0	1	0	0	1	1

Bit7: (Not used)

Bit[6:0]: IU0 (6:0) Main Current at Ambient Level for 0h

	rent at Ambient Lev		
"0000000":	0.2 mA	"1000000":	13.0 mA
"0000001":	0.4 mA	"1000001":	13.2 mA
"0000010":	0.6 mA	"1000010":	13.4 mA
"0000011":	0.8 mA	"1000011":	13.6 mA
"0000100":	1.0 mA	"1000100":	13.8 mA
"0000101":	1.2 mA	"1000101":	14.0 mA
"0000110":	1.4 mA	"1000110":	14.2 mA
"0000111":	1.6 mA	"1000111":	14.4 mA
"0001000":	1.8 mA	"1001000":	14.6 mA
"0001001":	2.0 mA	"1001001":	14.8 mA
"0001010":	2.2 mA	"1001010":	15.0 mA
"0001011":	2.4 mA	"1001011":	15.2 mA
"0001100":	2.6 mA	"1001100":	15.4 mA
"0001101":	2.8 mA	"1001101":	15.6 mA
"0001110":	3.0 mA	"1001110":	15.8 mA
"0001111":	3.2 mA	"1001111":	16.0 mA
"0010000":	3.4 mA	"1010000":	16.2 mA
"0010001":	3.6 mA	"1010001" :	16.4 mA
"0010010":	3.8 mA	"1010010" :	16.6 mA
"0010011" :	4.0 mA (Initial Value)	"1010011" :	16.8 mA
"0010100":	4.2 mA	"1010100" :	17.0 mA
"0010101":	4.4 mA	"1010101":	17.2 mA
"001010":	4.6 mA	"1010101":	17.4 mA
"0010110":	4.8 mA	"1010110":	17.4 mA
"0011000":	5.0 mA	"1011000":	17.8 mA
"0011000":	5.2 mA	"1011000":	18.0 mA
"0011001":	5.4 mA	"1011001":	18.2 mA
"0011010":	5.6 mA	"1011010":	18.4 mA
"0011011":	5.8 mA	"10111011":	18.6 mA
"0011100":		"1011100":	
"0011101":	6.0 mA 6.2 mA	"1011101":	18.8 mA 19.0 mA
"0011110":	6.4 mA	"1011110":	19.0 mA
"0100000":	6.6 mA	"1100000":	19.4 mA
"0100000":	6.8 mA	"1100000":	19.4 mA
"0100001":	7.0 mA	"1100001":	19.8 mA
"0100010":	7.0 IIIA 7.2 mA	"1100010":	
"0100011":		"1100011":	20.0 mA
"0100100 : "0100101" :	7.4 mA	"1100100 :	20.2 mA
"0100101 : "0100110" :	7.6 mA	"1100101 : "1100110" :	20.4 mA
	7.8 mA		20.6 mA
"0100111":	8.0 mA	"1100111":	20.8 mA
"0101000" : "0101001" :	8.2 mA	"1101000":	21.0 mA
"0101001":	8.4 mA	"1101001":	21.2 mA
"0101010 : "0101011" :	8.6 mA	"1101010":	21.4 mA
	8.8 mA	"1101011":	21.6 mA
"0101100" : "0101101" :	9.0 mA	"1101100" : "1101101" :	21.8 mA
	9.2 mA		22.0 mA
"0101110" : "0101111" :	9.4 mA	"1101110" : "1101111" :	22.2 mA
	9.6 mA		22.4 mA
"0110000":	9.8 mA	"1110000":	22.6 mA
"0110001":	10.0 mA	"1110001":	22.8 mA
"0110010":	10.2 mA	"1110010":	23.0 mA
"0110011":	10.4 mA	"1110011":	23.2 mA
"0110100":	10.6 mA	"1110100":	23.4 mA
"0110101":	10.8 mA	"1110101":	23.6 mA
"0110110":	11.0 mA	"1110110":	23.8 mA
"0110111":	11.2 mA	"1110111":	24.0 mA
"0111000":	11.4 mA	"1111000":	24.2 mA
"0111001":	11.6 mA	"1111001":	24.4 mA
"0111010":	11.8 mA	"1111010":	24.6 mA
"0111011":	12.0 mA	"1111011":	24.8 mA
"0111100" :	12.2 mA	"1111100":	25.0 mA
"0111101":	12.4 mA	"1111101":	25.2 mA
"0111110":	12.6 mA	"1111110":	25.4 mA
"0111111":	12.8 mA	"1111111":	25.6 mA

Address 0Eh < LED Max Current setting >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
0Eh	W	-	IU1 (6)	IU1 (5)	IU1 (4)	IU1 (3)	IU1 (2)	IU1 (1)	IU1 (0)
Initial Value	63h	-	1	1	0	0	0	1	1

Bit7: (Not used)

Bit[6:0]: IU1 (6:0) LED Max Current (for ALC)

	Current (IOI ALC)		
"0000000":	0.2 mA	"1000000":	13.0 mA
"0000001":	0.4 mA	"1000001":	13.2 mA
"0000010":	0.6 mA	"1000010":	13.4 mA
"0000011":	0.8 mA	"1000011":	13.6 mA
"0000100":	1.0 mA	"1000100" :	13.8 mA
"0000101":	1.2 mA	"1000101":	14.0 mA
"0000110":	1.4 mA	"1000110":	14.2 mA
"0000111":	1.6 mA	"1000111":	14.4 mA
"0001000":	1.8 mA	"1001000":	14.6 mA
"0001001":	2.0 mA	"1001001" :	14.8 mA
"0001001":	2.2 mA	"1001010" :	15.0 mA
"0001011":	2.4 mA	"1001011":	15.2 mA
"0001100":	2.6 mA	"1001100" :	15.4 mA
"0001101":	2.8 mA	"1001101":	15.6 mA
"0001110":	3.0 mA	"1001110":	15.8 mA
"0001111":	3.2 mA	"1001111":	16.0 mA
"0010000":	3.4 mA	"1010000":	16.2 mA
"0010001":	3.6 mA	"1010001" :	16.4 mA
"0010010":	3.8 mA	"1010010":	16.6 mA
"0010011":	4.0 mA	"1010011":	16.8 mA
"0010100":	4.2 mA	"1010100":	17.0 mA
"0010101":	4.4 mA	"1010101":	17.2 mA
"001010":	4.6 mA	"1010101":	17.4 mA
"0010111":	4.8 mA	"1010111":	17.6 mA
"0011000":	5.0 mA	"1011000":	17.8 mA
"0011001":	5.2 mA	"1011001" :	18.0 mA
"0011010":	5.4 mA	"1011010":	18.2 mA
"0011011":	5.6 mA	"1011011":	18.4 mA
"0011100" :	5.8 mA	"1011100" :	18.6 mA
			18.8 mA
"0011101":	6.0 mA	"1011101":	
"0011110":	6.2 mA	"1011110":	19.0 mA
"0011111":	6.4 mA	"1011111":	19.2 mA
"0100000":	6.6 mA	"1100000":	19.4 mA
"0100001":	6.8 mA	"1100001":	19.6 mA
"0100010":	7.0 mA	"1100010":	19.8 mA
"0100010":	7.2 mA	"1100010":	20.0 mA (Initial Value)
			20.0 TIA (initial value)
"0100100" :	7.4 mA	"1100100":	20.2 mA
"0100101" :	7.6 mA	"1100101":	20.4 mA
"0100110" :	7.8 mA	"1100110":	20.6 mA
"0100111":	8.0 mA	"1100111":	20.8 mA
"0101000":	8.2 mA	"1101000":	21.0 mA
"0101001":	8.4 mA	"1101001":	21.2 mA
"0101010":	8.6 mA	"1101010":	21.4 mA
"0101011":	8.8 mA	"1101011":	21.6 mA
"0101100":	9.0 mA	"1101100" :	21.8 mA
"0101101":	9.2 mA	"1101101":	22.0 mA
"0101110":	9.4 mA	"1101110":	22.2 mA
"0101111":	9.6 mA	"1101111":	22.4 mA
"0110000" :	9.8 mA	"1110000" :	22.6 mA
"0110000":		"1110001":	
	10.0 mA	"4440040"	22.8 mA
"0110010":	10.2 mA	"1110010":	23.0 mA
"0110011" :	10.4 mA	"1110011":	23.2 mA
"0110100":	10.6 mA	"1110100":	23.4 mA
"0110101":	10.8 mA	"1110101":	23.6 mA
"0110110":	11.0 mA	"1110110":	23.8 mA
"0110111":	11.2 mA	"1110111":	24.0 mA
"0110111 .		"4444000":	
"0111000":	11.4 mA	"1111000":	24.2 mA
"0111001":	11.6 mA	"1111001":	24.4 mA
"0111010" :	11.8 mA	"1111010":	24.6 mA
"0111011":	12.0 mA	"1111011" :	24.8 mA
"0111100":	12.2 mA	"1111100":	25.0 mA
"0111101" :	12.4 mA	"1111101":	25.2 mA
"011110":	12.6 mA	"1111110":	25.4 mA
"0111111" :	12.8 mA	"1111111":	25.6 mA

Address 13h < LDO Power Control>

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
13h	W/R	-	-	-	-	LDO4EN	LDO3EN	LDO2EN	LDO1EN
Initial Value	00h	-	-	-	-	0	0	0	0

Bit[7:4] : (Not used)

Bit3: LDO4EN LDO4 control (ON/OFF)

"0": LDO4 OFF (Initial Value)

"1": LDO4 ON

Bit2: LDO3EN LDO3 control (ON/OFF)

"0": LDO3 OFF (Initial Value)

"1": LDO3 ON

Bit1: LDO2EN LDO2 control (ON/OFF)

"0": LDO2 OFF (Initial Value)

"1": LDO2 ON

Bit0: LDO1EN LDO1 control (ON/OFF)

"0": LDO1 OFF (Initial Value)

"1": LDO1 ON

Address 14h < LDO1 Vout Control, LDO2 Vout Control >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
14h	R/W	LDO2VSEL3	LDO2VSEL2	LDO2VSEL1	LDO2VSEL0	LDO1VSEL3	LDO1VSEL2	LDO1VSEL1	LDO1VSEL0
Initial Value	74h	0	1	1	1	0	1	0	0

Bit[7:4]: LDO2VSEL [3:0]

"0000": 1.20 V "0001": 1.30 V "0010": 1.50 V "0011": 1.60 V "0100": 1.80 V "0101": 2.20 V "0110": 2.40 V

"0111": 2.50 V (Initial Value)

"1000": 2.60 V "1001": 2.70 V "1010": 2.80 V "1011": 2.90 V "1100": 3.00 V "1101": 3.10 V "1110": 3.20 V "1111": 3.30 V

Bit[3:0]: LDO1VSEL [3:0]

"0000": 1.20 V "0001": 1.30 V "0010": 1.50 V "0011": 1.60 V

"0100": 1.80 V (Initial Value)

"0101": "0110": 2.20 V 2.40 V "0111": 2.50 V "1000": 2.60 V "1001": 2.70 V "1010": 2.80 V "1011": 2.90 V "1100": 3.00 V "1101": 3.10 V "1110": 3.20 V "1111": 3.30 V Address 15h < LDO3 Vout Control, LDO4 Vout Control >

Address	R/W	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
15h	R/W	LDO4VSEL3	LDO4VSEL2	LDO4VSEL1	LDO4VSEL0	LDO3VSEL3	LDO3VSEL2	LDO3VSEL1	LDO3VSEL0
Initial Value	A4h	1	0	1	0	0	1	0	0

```
Bit[7:4]: LDO4VSEL [3:0]
             "0000":
                       1.20 V
             "0001":
"0010":
                        1.30 V
                        1.50 V
             "0011":
                        1.60 V
             "0100":
                        1.80 V
             "0101":
                        2.20 V
             "0110":
                        2.40 V
             "0111":
                        2.50 V
             "1000":
                        2.60 V
             "1001":
                        2.70 V
             "1010":
                        2.80 V
                                 (Initial Value)
             "1011":
                        2.90 V
             "1100":
                        3.00 V
             "1101":
                        3.10 V
            "1110":
"1111":
                        3.20 V
                        3.30 V
Bit[3:0]: LDO3VSEL [3:0]
             "0000":
                       1.20 V
             "0001":
                        1.30 V
             "0010":
                        1.50 V
             "0011":
                        1.60 V
             "0100":
"0101":
                                 (Initial Value)
                        1.80 V
                        2.20 V
             "0110":
                        2.40 V
             "0111":
                        2.50 V
             "1000":
                        2.60 V
             "1001":
"1010":
                        2.70 V
                        2.80 V
            "1011":
"1100":
                        2.90 V
                        3.00 V
             "1101":
                        3.10 V
             "1110":
                        3.20 V
```

"1111":

3.30 V

Reset

There are two kinds of reset, software reset and hardware reset

- (1)Software reset
 - All the registers are initialized by SFTRST="1".
 - SFTRST is an automatically returned to "0". (Auto Return 0).

(2) Hardware reset

- · It shifts to hardware reset by changing RESETB pin "H" → "L".
- The condition of all the registers under hardware reset pin is returned to the Initial Value, and it stops accepting all address.
- It's possible to release from a state of hardware reset by changing RESETB pin "L" → "H".
- RESETB pin has delay circuit. It doesn't recognize as hardware reset in "L" period under 5µs.

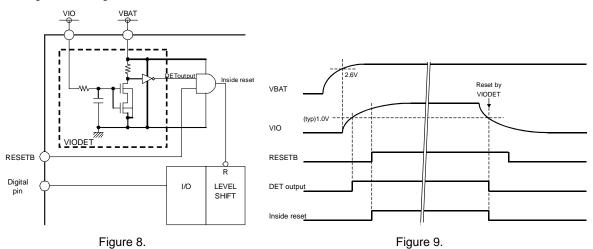
(3) Reset Sequence

• When hardware reset was done during software reset, software reset is canceled when hardware reset is canceled. (Because the Initial Value of software reset is "0")

VIODET

The decline of the VIO voltage is detected, and faulty operation inside the IC is prevented by giving resetting to Levelsift block

Image Block Diagram



When the VIO voltage becomes more than typ1.0V(Vth of NMOS in the IC), VIODET is removed.

On the contrary, when VIO is as follows 1.0V, it takes reset.(The VBAT voltage being a prescribed movement range)

●Thermal Shut Down

A thermal shutdown function is effective in the following block.

DC/DC (Charge Pump)

LED Driver

SBIAS

LDO1, LDO2, LDO3, LDO4

The thermal shutdown function is detection temperature that it works is about 195°C.

Detection temperature has a hysteresis, and detection release temperature is about 175 °C.

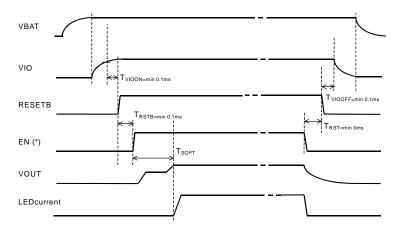
(Design reference value)

●DC / DC

Start

DC/DC circuit operates when any LED turns ON. (DCDCFON=0)

When the start of the DC/DC circuit is done, it has the soft start function to prevent a rush current. Force of VBAT and VIO is to go as follows.



(*) An EN signal means the following in the upper figure.

EN = "MLEDEN" or "W * EN"

(= LED The LED lighting control of a setup of connection VOUT)

But, as for $Ta > T_{TSD}$ ($typ : 195^{\circ}$ C), a protection function functions, and an EN signal doesn't become effective.

 $\mathsf{T}_{\mathsf{SOFT}}$ changes by the capacitor connected to VOUT and inside OSC.

T_{SOFT} is Typ 200 μ s (when the output capacitor of VOUT =1.0 μ F).

Figure 10.

Over Voltage protection / Over Current protection

DC/DC circuit output (VOUT) is equipped with the over-voltage protection and the over current protection function.

A VOUT over-voltage detection voltage is about 5.6V(typ). (VOUT at the time of rise in a voltage)

A detection voltage has a hysteresis, and a detection release voltage is about 5.4V (typ).

And, when VOUT output short to ground, input current of the battery terminal is limited by an over current protection function.

Mode transition

The transition of boosts multiple transits automatically by VBAT Voltage and the VOUT Pin Voltage.

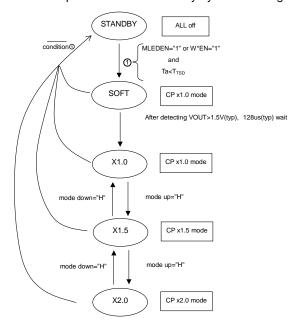


Figure11.

The mode transition of the charge pump works as follows.

<x1.0→x1.5→x2.0 Mode transition>

The transition of the mode is done when VOUT was compared with VBAT and the next condition was satisfied.

```
x1.0→x1.5 Mode transition

VBAT ≤ VOUT + (Ron10×lout)

(LED Pin feedback:VOUT = Vf+0.2(Typ))

x1.5→x2.0 Mode transition

VBAT×1.5 ≤ VOUT +(Ron15×lout)

(LED Pin feedback:VOUT = Vf+0.2(Typ))
```

Ron10: x1 Charge pump on resistance 1.4 Ω (Typ) Ron15: x1.5 Charge pump on resistance 8.5 Ω (Typ)

<x2.0→x1.5→x1.0 Mode transition>

The transition of the mode is done when the ratio of VOUT and VBAT is detected and it exceeds a fixed voltage ratio.

```
x1.5→x1.0 Mode transition

VBAT / VOUT =1.16(Design value)

x2.0→x1.5 Mode transition

VBAT / VOUT =1.12(Design value)
```

●LED Driver

The LED driver of 6ch is constructed as the ground plan. Equivalence control is possible with LED1 - 4(LED4 can choose use/un-use with a register W4MD.).

LED5, LED6 is controllable individually.

As for LED5, LED6, grouping setting to the main control is possible, and main control becomes effective for the main group in the allotment. LED5 and LED6 are setups of grouping to the main control.

When LED5 and LED6 are used by the individual control, a slope time setup (register THL and TLH) doesn't become effective.

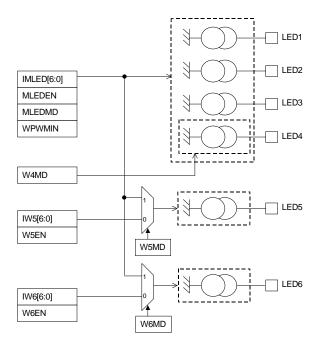


Figure 12.

LED Composition which can be set up is the following.

The main, other1 and other2 are controllable to each. (Enable and current setting)

Main (ALC,PWM)	Other1	Other2
6LEDs	-	-
5LEDs	-	-
5LEDs	1LED	-
4LEDs	-	-
4LEDs	1 LED	-
4LEDs	2 LEDs	-
4LEDs	1 LED	1LED
3LEDs	-	-
3LEDs	1 LED	-
3LEDs	2 LEDs	-
3LEDs	1 LED	1LED

● ALC (Auto Luminous Control)

- LCD backlight current adjustment is possible in the basis of the data detected by external ambient light sensor.
- Extensive selection of the ambient light sensors (Photo Diode, Photo Transistor, Photo IC(linear / logarithm)) is possible by building adjustment feature of Sensor bias, gain adjustment and offset adjustment.
- Ambient data is changed into ambient level by digital data processing, and it can be read through I²C I / F.
- · Register setting can customize a conversion to LED current. (Initial Value is pre-set.)
- Natural dimming of LED driver is possible with the adjustment of the current transition speed.

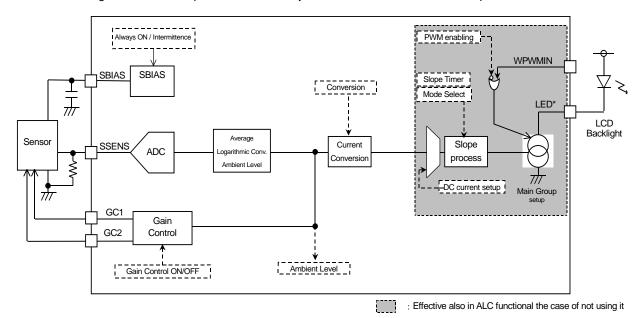


Figure 13.

(1) Auto Luminous Control ON/OFF

- · ALC block can be independent setting ON/OFF.
- It can use only to measure the Ambient level.

Register: ALCEN Register: MLEDEN Register: MLEDMD

Refer to under about the associate ALC mode and Main LED current.

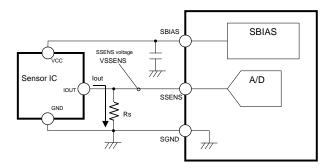
ALCEN	MLEDEN	MLEDMD	Sensor I/F	LED control	Mode	Main LED current
0	0	х	055	OFF	OFF	-
0	1	0	OFF (AMB(3:0)=0h)	ON	Non ALC made	IMLED(6:0)
0	1	1	(AIVID(3.0)=011)	ON	Non ALC mode	IU0(6:0) (*1)
1	0	х		OFF		-
1	1	0	ON	ON	ALC mode	IMLED(6:0)
1	1	1		ON		ALC mode (*2)

^(*1) At this mode, because Sensor I/F is OFF, AMB(3:0)=0h. So, Main LED current is selected IU0(6:0).

^(*2) At this mode, Main LED current is calculated (See(8)Convert LED Current) It becomes current value corresponding to each brightness.

(2) I/V conversion

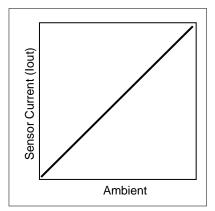
 External resistance for the I-V conversion (Rs) are adjusted with adaptation of sensor characteristic

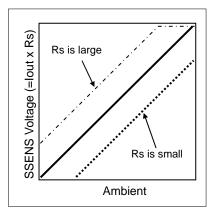


Rs : Sense resistance (A sensor output current is changed into the voltage value.) SBIAS : Bias power supply terminal for the sensor SSENS : Sense voltage input terminal

SSENS Voltage = Iout x Rs

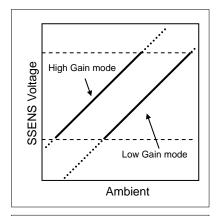
Figure 14.

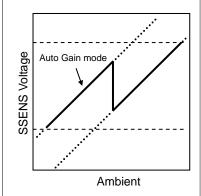




(3) Gain control

- Sensor gain switching function is built in to extend the dynamic range.
- It is controlled by register setup.
- When automatic gain control is off, the gain status can be set upin the manual. Register: GAIN
- GC1 and GC2 are outputted corresponding to each gain status.





	Example 1 (Use	BH1621FVC)	Exam	ple 2	Example 3	
Application example	SBIAS VCC IOUT SSENS BH1621 GC1 GC1 GC2 GC2 SGND		SBIAS SSENS SSENS GC1 GC2 SGND Resistor values are relative		SBIAS SSENS SSENS GC1 GC2 SGND	
Operating mode	Au	to	Αι	ıto	Fixed	
GAIN setting	C)	()	1	
Gain status	High Low		High	Low	-	
GC1 output	Т	L	片	L	Л	
GC2 output	L	Л	L	Л	L	

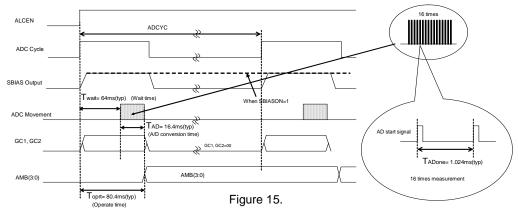
Π: This means that it becomes High with A/D measurement cycle synchronously.

(*1): Set up the relative ratio of the resistance in the difference in the brightness change of the High Gain mode and the Low Gain mode carefully.

(4) A/D conversion

- The detection of ambient data is done periodically for the low power.
- SBIAS and ADC are turned off except for the ambient measurement.
- The sensor current may be shut in this function, it can possible to decrease the current consumption.
- · SBIAS pin and SSENS pin are pull-down in internal when there are OFF.
- · SBIAS circuit has the two modes. (Usually ON mode or intermittent mode)

Register: ADCYC Register: SBIASON



(5) Average filter

· Average filter is built in to rid noise or flicker. Average is 16 times

(6) Ambient level detection

- · Averaged A/D value is converted to Ambient level corresponding to Gain control.
- Ambient level is judged to rank of 16 steps by ambient data.
- Ambient level is output through I²C.

Register: AMB(3:0)

GAIN		0	1			
GAIN Setting	Low	High	-			
Ambient Level		SSENS	SSENS Voltage			
0h	/	VoS×0/256	VoS×0/256			
1h		VoS × 1 / 256	VoS×1/256			
2h		VoS×2/256	VoS×2/256			
3h		VoS × 3 / 256 VoS × 4 / 256	VoS × 3 / 256 VoS × 4 / 256			
4h	\dashv /	VoS × 5 / 256	VoS × 4 / 256 VoS × 5 / 256			
411		VoS×7/256	VoS × 6 / 256			
5h	VoS×0/256	VoS × 8 / 256 VoS × 12 / 256	VoS×7 / 256 VoS×9 / 256			
6h	VoS×1/256	VoS × 13 / 256	VoS × 10 / 256			
	VoS×2/256	VoS × 21 / 256 VoS × 22 / 256	VoS × 13 / 256 VoS × 14 / 256			
7h	VoS × 3 / 256	VoS × 37 / 256	VoS × 14 / 256 VoS × 19 / 256			
8h	VoS × 4 / 256	VoS × 38 / 256	VoS × 20 / 256			
OH	VoS×6/256	VoS × 65 / 256	VoS × 27 / 256			
9h	VoS×7 / 256 VoS×11 / 256	VoS × 66 / 256 VoS × 113 / 256	VoS × 28 / 256 VoS × 38 / 256			
Λ L	VoS × 12 / 256	VoS × 114 / 256	VoS × 39 / 256			
Ah	VoS × 20 / 256	VoS × 199 / 256	VoS × 53 / 256			
Bh	VoS × 21 / 256	VoS × 200 / 256	VoS × 54 / 256			
<u> </u>	VoS × 36 / 256	VoS × 255 / 256	VoS × 74 / 256			
Ch	VoS × 37 / 256		VoS × 75 / 256			
OII	VoS × 64 / 256		VoS × 104 / 256			
Dh	VoS × 65 / 256		VoS × 105 / 256			
ווט	VoS × 114 / 256		VoS × 144 / 256			
Eh	VoS × 115 / 256		VoS × 145 / 256			
<u> </u>	VoS × 199 / 256		VoS × 199 / 256			
Fh	VoS × 200 / 256		VoS×200/256			
ГП	VoS × 255 / 256		VoS × 255 / 256			

*In the Auto Gain control mode, sensor gain changes in gray-colored ambient level.

*"/": This means that this zone is not outputted in this mode.

(7)Convert LED Current

- LED current can be assigned as each of 16 steps of the ambient level.
- Convert LED Current by Min Current setting, Max Current setting, step setting and curbu setting.

Register: IU0 IU1 CRV STEP [2:0]

Conversion Table (Initial Value)

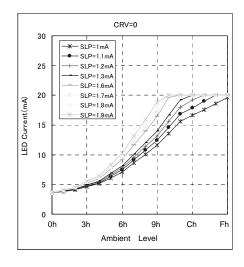
Ambient	Coeff	icient	Ambient	Coeff	icient
Level	CRV=0	CRV=1	Level	CRV=0	CRV=1
0h	0	0	8h	6.5	8
1h	0.25	1	9h	8	9
2h	0.5	2	Ah	10	10
3h	1	3	Bh	12	11
4h	1.5	4	Ch	13	12
5h	2.5	5	Dh	14	13
6h	3.5	6	Eh	15	14
7h	5	7	Fh	16	15

Step Table Setting				
STEP[2:0]	ΔΙ			
000	1.0mA			
001	1.1mA			
010	1.2mA			
011	1.3mA			
100	1.6mA			
101	1.7mA			
110	1.8mA			
111	1.9mA			

 $I = \Delta I \times Coefficient+IU0$

- ※ I≧IU1:I=IU1

The example of a setting IU0=4mA IU1=20mA





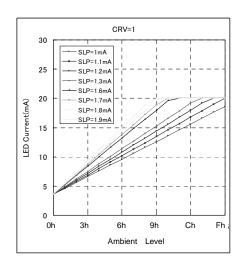


Figure 17.

(8) Slope process

- Slope process is given to LED current to dim naturally.
- · LED current changes in the 256Step gradation in sloping.
- Up(dark→bright),Down(bright→dark) LED current transition speed are set individually.

Register: TLH (3:0) Register: TLH (3:0)

Main LED current changes as follows at the time as the slope.
 TLH (THL) is setup of time of the current step 2/256.

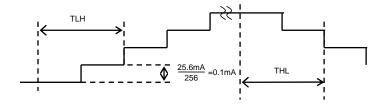


Figure 18.

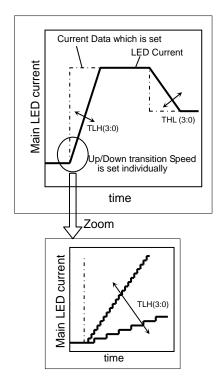


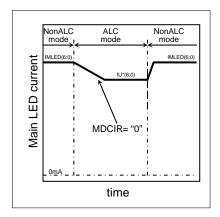
When mode is changed (ALC↔Non ALC),

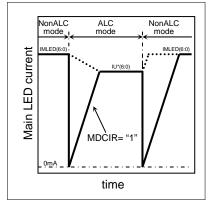
it can select the way to sloping.

Register: MDCIR

"0": LED current non-reset when mode change "1": LED current reset when mode change







(10) Current adjustment

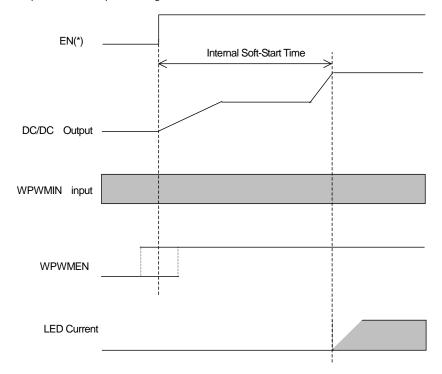
When the register setting permits it, PWM drive by the external terminal (WPWMIN) is possible.
 Register: WPWMEN

• It is suitable for the intensity correction by external control, because PWM based on Main LED current of register

setup or ALC control.

WPWMEN (Register)	WPWMIN(External Pin)	Main group LED current	
0	L	Normal operation	
0	Н	Normal operation	
1	L	Forced OFF	
'	Н	Normal operation	

[&]quot; Normal operation " depends on the setup of each register.



 $\mathsf{EN}(^*)$: it means "MLEDEN" or "W*EN".

It is possible to make it a WPWMIN input and WPWMEN=1 in front of EN(*). A PWM drive becomes effective after the time of an LED current standup.

When rising during PWM operation, as for the standup time of a DC/DC output, only the rate of PWM Duty becomes late. Appearance may be influenced when extremely late frequency and extremely low Duty are inputted.

Please secure 250 µs or more of H sections at the time of PWM pulse Force.

Figure 19.

●I/O

When the RESETB pin is Low, the input buffers (SDA and SCL) are disabling for the Low consumption power.

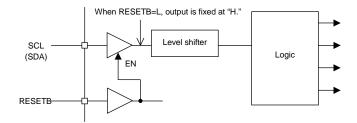


Figure 20.

Special care should be taken because a current path may be formed via a terminal protection diode, depending on an I/O power-on sequence or an input level.

About the Start of LDO1 to LDO4

It must start as follows.

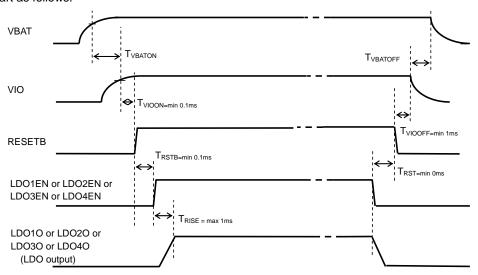


Figure 21.

<Start Sequence>

VBAT ON (Enough rise up) \rightarrow VIO ON (Enough rise up) \rightarrow Reset release \rightarrow LDO ON (Register access acceptable)

<End Sequence>

LDO OFF → Reset → VIO OFF (Enough fall down) → VBAT OFF

● About the Pin Management of the Function that Isn't Used and Test Pins

Setting it as follows is recommended with the test pin and the pin which isn't used.

Set up pin referring to the "Equivalent circuit diagram" so that there may not be a problem under the actual use.

T2,T4	Short to GND because pin for test input OPEN because pin for test output	
T1,T3		
Non-used LED Pin	Short to GND (Must) Don't set the register concerned with non-used LED Pin	
WPWMIN	Short to ground (A Pull-Down resistance built-in terminal is contained, too.)	

Operation Settings (Flow Example)

1. Backlight: Auto Luminous Mode

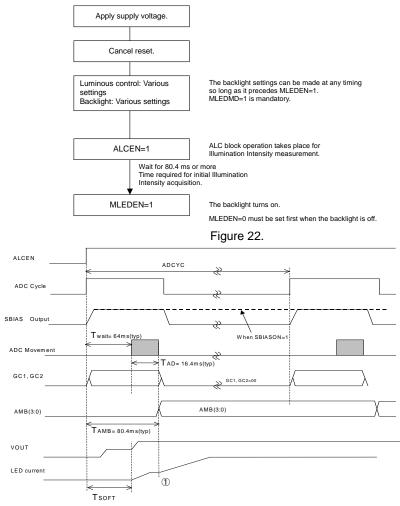
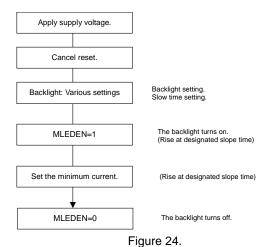


Figure 23

When It cannot wait for the first illumination measurement, backlight lighting is possible with ALCEN. But the extremely short case of slope rise time, a shoulder may be done like ① for an LED electric current. (To the first illumination measurement for AMB(3:0)=00h)

2. Backlight: Fade-in/Fade-out



3. Backlight without Auto Luminous Mode

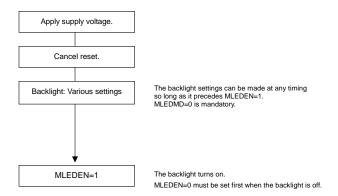


Figure 25.

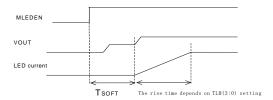


Figure 26.

● PCB Pattern of the Power Dissipation Measuring Board

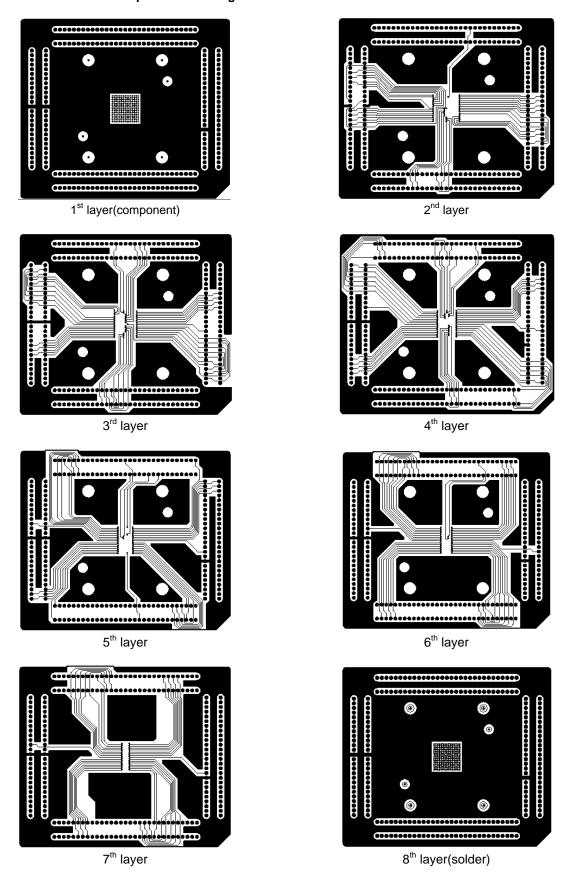
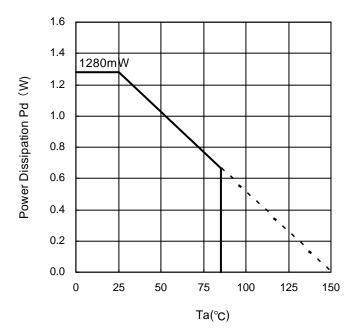


Figure 27. PCB Pattern of the Power Dissipation Measuring Board

● Power Dissipation (On the ROHM's Standard Board)



Information of the ROHM's standard board Material: glass-epoxy Size: 50mm × 58mm × 1.75mm(8th layer)

Figure 28. Power Dissipation

Wiring pattern figure Refer to page 38.

Operational Notes

(1) Absolute Maximum Ratings

An excess in the absolute maximum ratings, such as supply voltage, temperature range of operating conditions, etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

(2) Power supply and ground line

Design PCB pattern to provide low impedance for the wiring between the power supply and the ground lines. Pay attention to the interference by common impedance of layout pattern when there are plural power supplies and ground lines. Especially, when there are ground pattern for small signal and ground pattern for large current included the external circuits, please separate each ground pattern. Furthermore, for all power supply pins to ICs, mount a capacitor between the power supply and the ground pin. At the same time, in order to use a capacitor, thoroughly check to be sure the characteristics of the capacitor to be used present no problem including the occurrence of capacity dropout at a low temperature, thus determining the constant.

(3) Ground voltage

Make setting of the potential of the ground pin so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no pins are at a potential lower than the ground voltage including an actual electric transient.

(4) Short circuit between pins and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between pins or between the pin and the power supply or the ground pin, the ICs can break down.

(5) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

(6) Input pins

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input pin. Therefore, pay thorough attention not to handle the input pins, such as to apply to the input pins a voltage lower than the ground respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input pins when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input pins a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.

(7) External capacitor

In order to use a ceramic capacitor as the external capacitor, determine the constant with consideration given to a degradation in the nominal capacitance due to DC bias and changes in the capacitance due to temperature, etc.

(8) Thermal shutdown circuit (TSD)

This IC builds in a thermal shutdown (TSD) circuit. When junction temperatures become detection temperature or higher, the thermal shutdown circuit operates and turns a switch OFF. The thermal shutdown circuit, which is aimed at isolating the IC from thermal runaway as much as possible, is not aimed at the protection or guarantee of the IC. Therefore, do not continuously use the IC with this circuit operating or use the IC assuming its operation.

(9) Thermal design

Perform thermal design in which there are adequate margins by taking into account the permissible dissipation (Pd) in actual states of use.

(10) LDO

Use each output of LDO by the independence. Don't use under the condition that each output is short-circuited because it has the possibility that an operation becomes unstable.

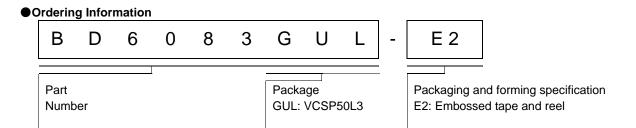
(11) About the rush current

For ICs with more than one power supply, it is possible that rush current may flow instantaneously due to the internal powering sequence and delays. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of wiring.

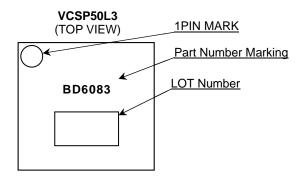
Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority.

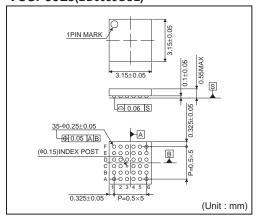


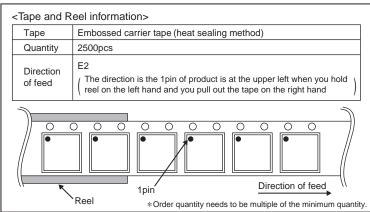
Marking Diagram



● Physical Dimension Tape and Reel Information

VCSP50L3(BD6083GUL)





●Revision History

Date	Revision	Changes	
05.Oct.2012	001	New Release	

Notice

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Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSIII	CLASS II b	CL ACCTI
CLASSIV		CLASSⅢ	CLASSIII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
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- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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