

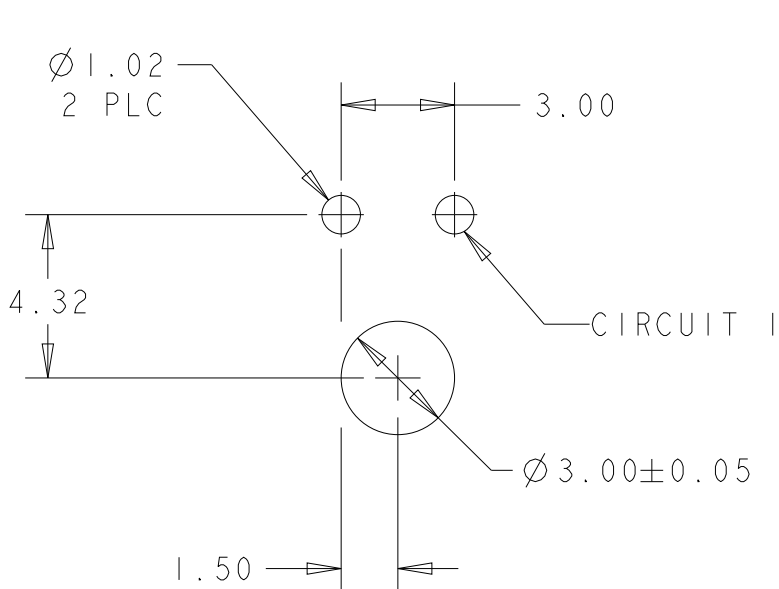
REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

D

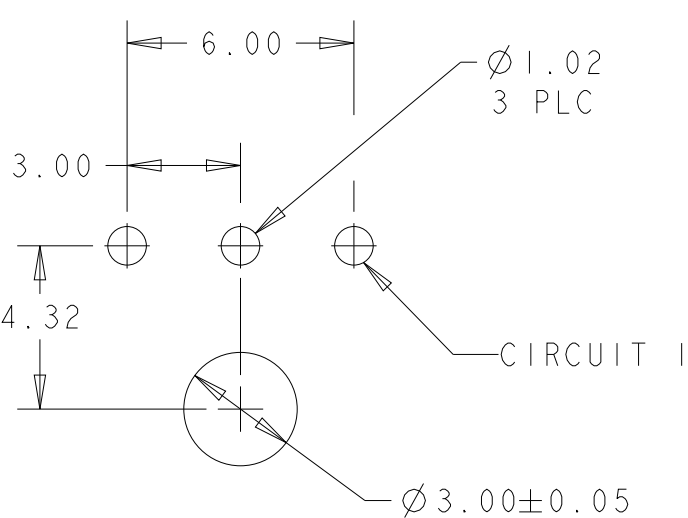
C

B

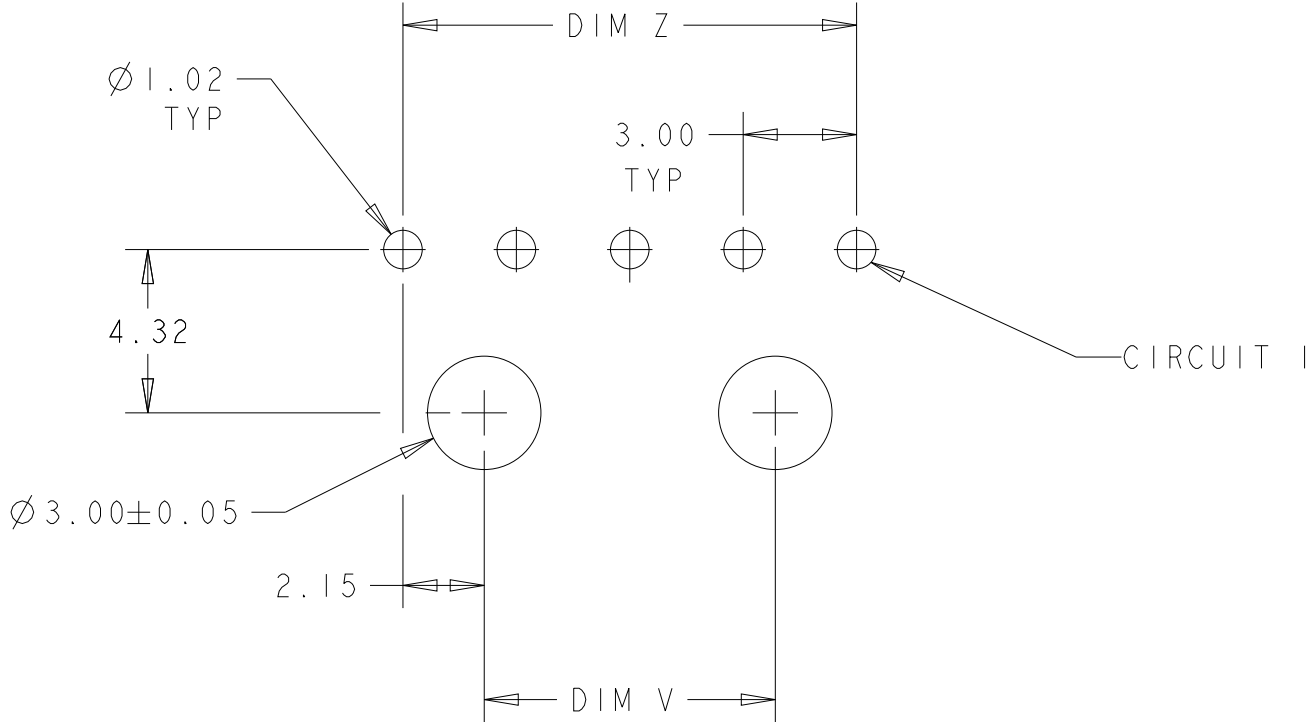
A



PCB LAYOUT FOR 2 POSITION 
COMPONENT SIDE



PCB LAYOUT FOR 3 POSITION 
COMPONENT SIDE



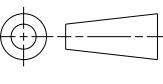
PCB LAYOUT FOR 4-12 POSITION 
COMPONENT SIDE

D

C

B

A

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN J.ROOT 06OCT2000	 TE Connectivity		
DIMENSIONS:		CHK K.WHITAKER 13-MAR-2001			
mm		APVD K.WHITAKER 13-MAR-2001	NAME RIGHT ANGLE THRU HOLEHEADER ASSY, TIN CONTACTS W/PLASTIC HOLDDOWNS, SINGLE ROW, MICRO MATE-N-LOK™		
		PRODUCT SPEC			
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APPLICATION SPEC	SIZE A200779		
0 PLC ± 1 PLC ± 2 PLC ±0.13 3 PLC ± 4 PLC ± ANGLES ± FINISH		WEIGHT			
SEE TABLE		CUSTOMER DRAWING	DRAWING NO C-1445055		
		SCALE 5:1	SHEET 2 OF 2		REV B1

Mouser Electronics

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