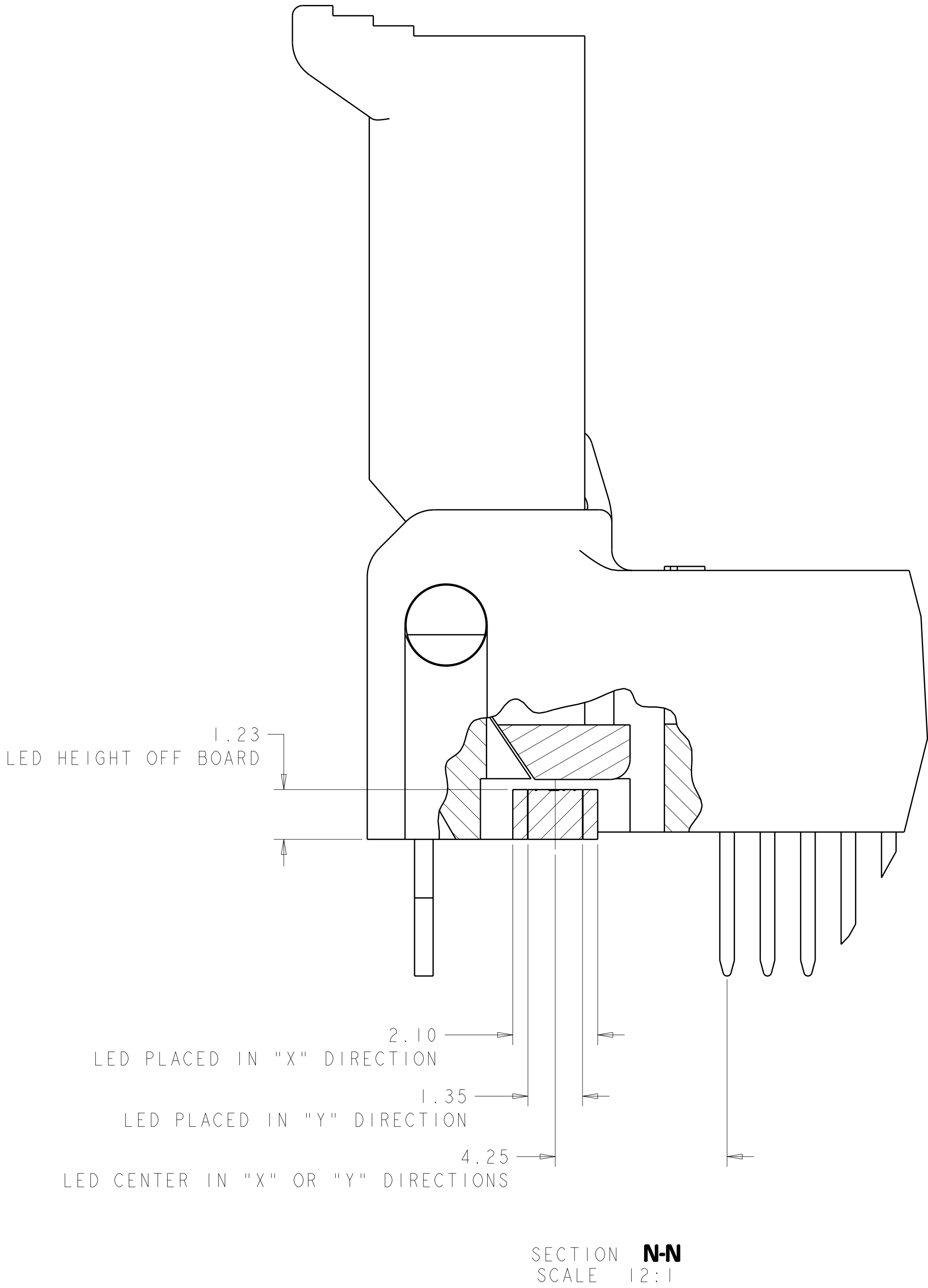
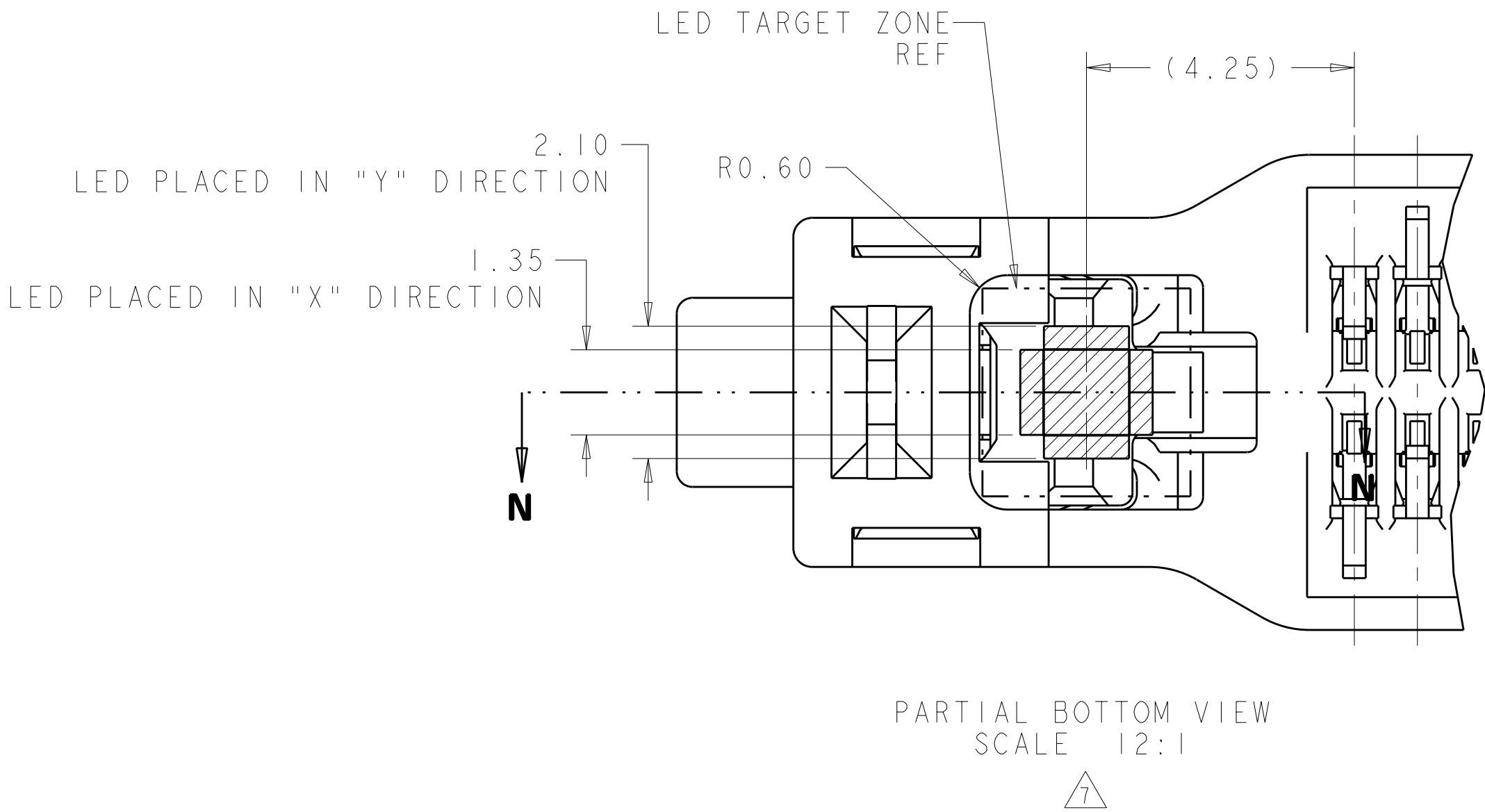


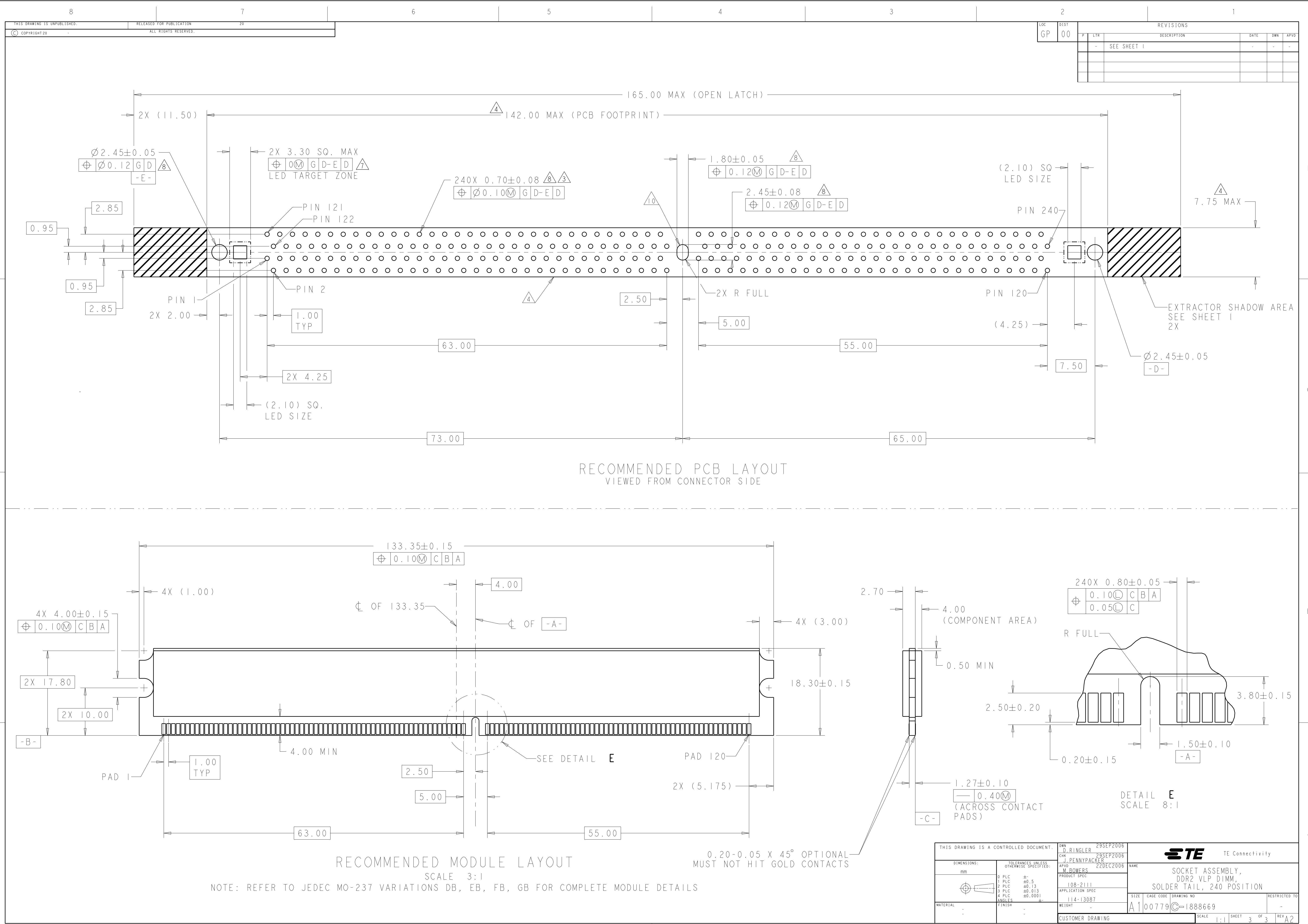


LOC		DIST		REVISIONS				
GP	00	P	LYR	DESCRIPTION	DATE	DWN	APVD	
				SEE SHEET 1	-	-	-	



THIS DRAWING IS A CONTROLLED DOCUMENT.				DWN	O. RINGLER 29SEP2006		 TE Connectivity		
				CHK	J. PENNYPACKER 29SEP2006				
				APVD	M. BOWERS 22DEC2006		NAME		
<div>DIMENSIONS:</div> <div>mm</div> <div></div>		<div>TOLERANCES, UNLESS OTHERWISE SPECIFIED:</div> <div>0 PLC +0.0</div> <div>1 PLC +0.0</div> <div>2 PLC +0.13</div> <div>3 PLC +0.013</div> <div>4 PLC +0.000</div> <div>ANGLES ±0.0001</div>		<div>PRODUCT SPEC</div> <div>108-2111</div> <div>APPLICATION SPEC</div> <div>114-13087</div> <div>WEIGHT</div> <div>-</div> <div>CUSTOMER DRAWING</div>		<div>SOCKET ASSEMBLY, DDR2 VLP DIMM, SOLDER TAIL, 240 POSITION</div>		RESTRICTED TO -	
MATERIAL		FINISH		SIZE		CAGE CODE		DRAWING NO	
-		-		A1		100779		C=1888669	
				SCALE		4:1		SHEET 2 of 3	
								REV A2	



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