

LOC		DIST		REVISIONS						
-	-	P	LTR	DESCRIPTION			DATE	DWN	APVD	
			B	REV PER ECO 12-010940			7-31-12	CT	DH	
			C	REV PER ECO-12-016528			20SEP2012	RJM	DH	
			D	ECO-13-004755			27MAR2013	KG	DH	
			E	ECO-14-017030			25NOV2014	MGM	CT	

⊕

⌀0.05

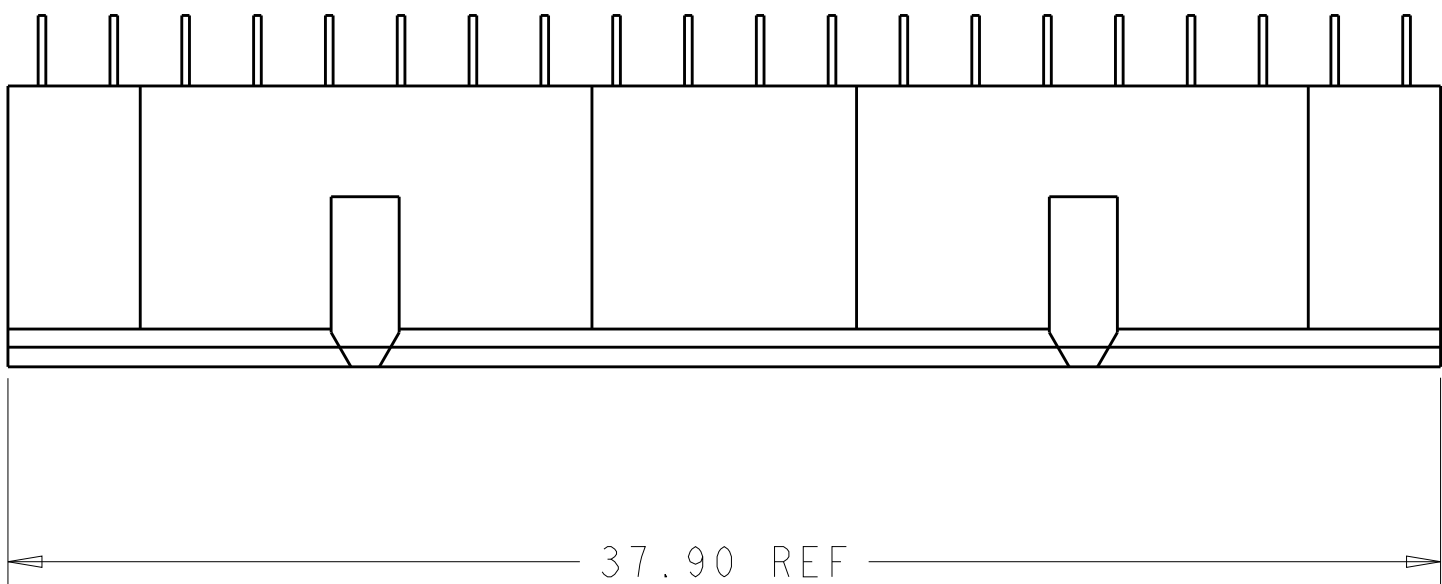
⊗

X

Y

FINISHED HOLE SIZE
AND PLATED THRU HOLES

CONNECTOR
OUTLINE



ROW I
ROW H
ROW G
ROW F
ROW E
ROW D
ROW C
ROW B
ROW A

20

1.85 REF

-Y- 6

BSC 6

1.60
TYP

12.80
REF

TOP OF DAUGHTER
CARD PCB

0.80 REF

0.90
REF

1.90
TYP

0.90
REF

BSC 6

36.10 REF

○ = SIGNAL
⊗ = GROUND

RECOMMENDED DIFFERENTIAL PAIR PC BOARD LAYOUT
COMPONENT SIDE SHOWN
SCALE 8:1

1 MATERIAL:
CONTACT - COPPER ALLOY
HOUSING - LCP, COLOR - BLACK

2 FINISH:
MATING AREA: 1.27μm MIN GOLD OVER
1.27μm MIN NICKEL

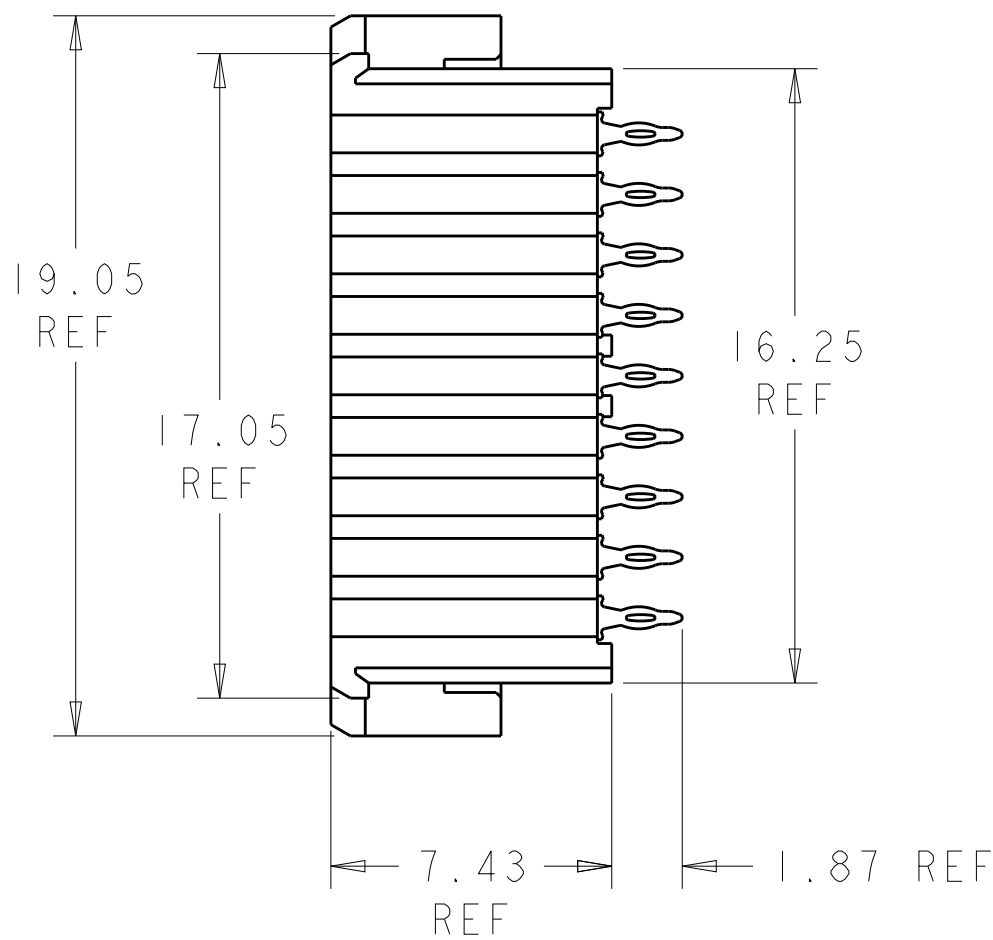
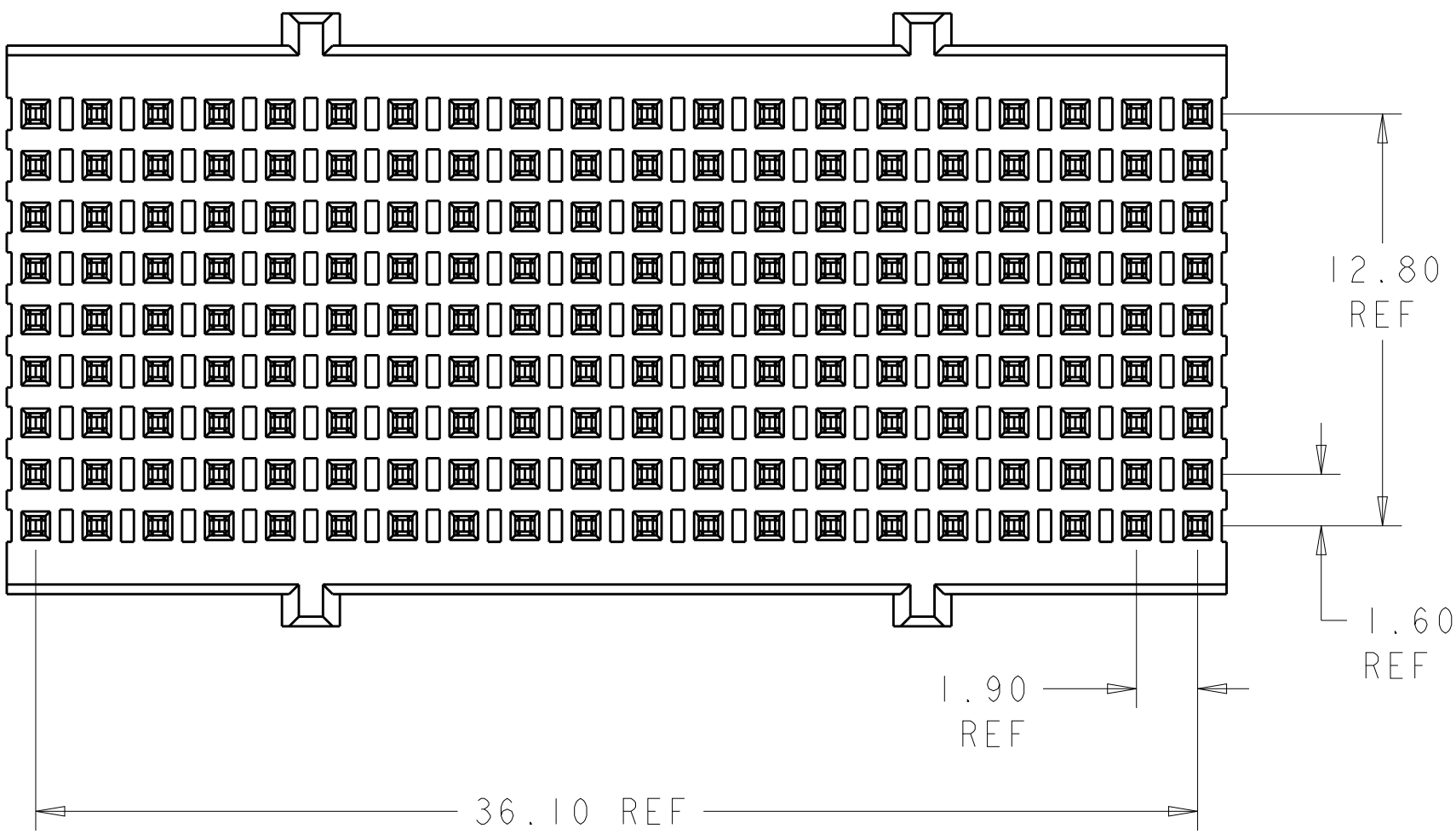
3 FINISH:
COMPLIANT PIN AREA: 0.38μm MIN BRIGHT TIN LEAD
OVER 1.27μm MIN NICKEL

4 FINISH:
COMPLIANT PIN AREA: 0.38μm MIN MATTE TIN
OVER 1.27μm MIN NICKEL

5 ---

6 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.

7 REFERENCE APPLICATION SPEC 114-13267 FOR RECOMMENDED
PLATING THRU HOLE DIAMETER AND PLATING THICKNESS.



2 4	2000905-2
2 3	2000905-1
FINISH	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT. DIMENSIONING AND TOLERANCING PER GPS (ISO STANDARDS)		DWN D. BELACK 06JUL2009		 TE Connectivity	
DIMENSIONS: mm		CHK A. TSANG		NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD M. McALONIS		PRODUCT SPEC	
0 PLC ±°		108-2409		Fortis Zd; 20-COLUMN, VERTICAL, CENTER MODULE ASSEMBLY	
1 PLC ±°		APPLICATION SPEC			
2 PLC ±°		114-13267		RESTRICTED TO	
3 PLC ±°		WEIGHT ~6.86g		SCALE 5:1	
4 PLC ±°		CUSTOMER DRAWING		SHEET 1 OF 1	
ANGLES ±2°		A100779C=2000905		REV E	
FINISH		-		-	
MATERIAL					

Mouser Electronics

Authorized Distributor

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[TE Connectivity:](#)

[2000905-1](#)