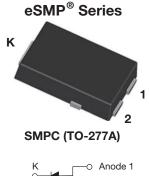
V15P45S-M3

Vishay General Semiconductor

SMD Photovoltaic Solar Cell Protection TMBS[®] (Trench MOS Barrier Schottky) Rectifiers

Ultra Low $V_F = 0.31$ V at $I_F = 5$ A



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Cathode O Anode 2

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DESIGN SUPPORT TOOLS



PRIMARY CHARACTERISTICS				
I _{F(AV)}	15 A			
V _{RRM}	45 V			
I _{FSM}	210 A			
V_F at $I_F = 15 A$	0.42 V			
T _J max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V15P45S	UNIT	
Device marking code		1545S		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	I _F ⁽¹⁾	15	Α	
Maximum DC forward current	I _F ⁽²⁾	4.8		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	210	A	
Junction temperature in DC forward current without reverse bias, $t \leq 1 \ h$	T _J ⁽³⁾	³⁾ ≤ 200		
Operating junction temperature range	T _{OP}	-40 to +150	°C	
Storage temperature range	T _{STG}	-40 to +175	°C	

Notes

⁽¹⁾ Mounted on 30 mm x 30 mm aluminum PCB

⁽²⁾ Free air, mounted on recommended copper pad area

⁽³⁾ Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5.0 A	T _A = 25 °C		0.40	-	V
	I _F = 7.5 A			0.45	-	
	I _F = 15 A			0.49	0.58	
	I _F = 5.0 A	T _A = 125 °C		0.31	-	
	I _F = 7.5 A			0.34	-	
	I _F = 15 A			0.42	0.51	
Reverse current	V _R = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	1500	μA
	$v_{\rm R} = 45 v$	T _A = 125 °C	IR (=/	15	50	mA

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V15P45S	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	75	°C/W	
rypical mermanesistance	R _{0JM} ⁽²⁾	4		

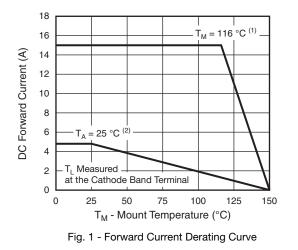
Notes

⁽¹⁾ Free air, mounted on recommended copper pad area; thermal resistance R_{0JA} - junction to ambient

⁽²⁾ Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V15P45S-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V15P45S-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



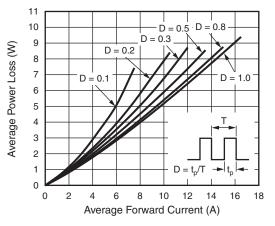


Fig. 2 - Forward Power Loss Characteristics Per Diode

Notes

- ⁽¹⁾ Mounted on 30 mm x 30 mm aluminum PCB; T_M measured at the terminal of cathode band ($R_{\theta JM} = 4 \text{ °C/W}$)
- $^{(2)}$ Free air, mounted on recommended copper pad area (R_{θ JA} = 75 °C/W)



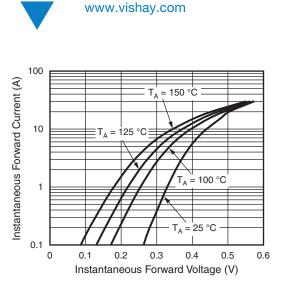


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

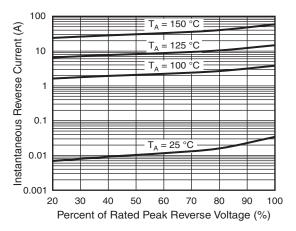


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

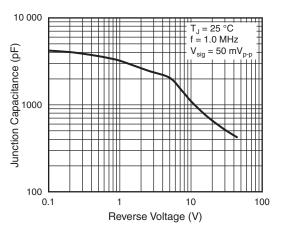


Fig. 5 - Typical Junction Capacitance

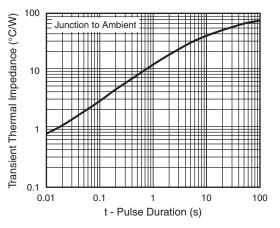
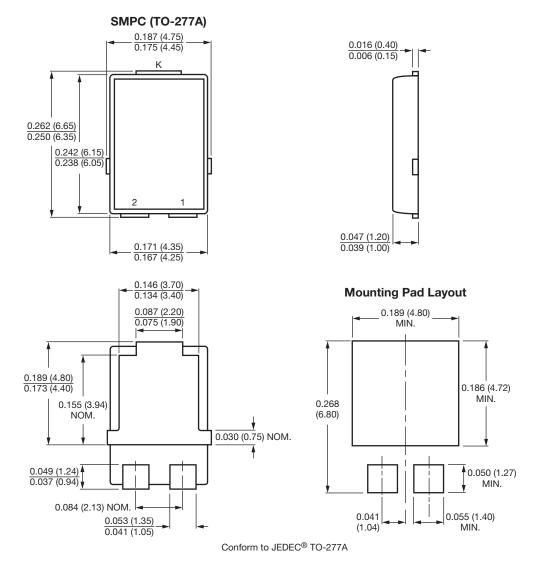


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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