



Description

- LVDS output crystal oscillator using a high frequency fundamental crystal to give very low jitter. Hermetically sealed ceramic package with a seam sealed metal lid.
- Model IQXO-660
- Model Issue number 2

Frequency Parameters

- Frequency 148.50MHz
- Frequency Stability $\pm 50.00\text{ppm}$
- Operating Temperature Range -10.00 to 70.00°C
- Ageing $\pm 3\text{ppm}$ max per year at 25°C

Electrical Parameters

- Supply Voltage $3.3\text{V} \pm 5\%$
- Current Draw 50.000mA

Output Details

- Output Compatibility LVDS
- Drive Capability 100Ω
- Rise & Fall Time (20 - 80%) 0.5ns max
- Duty Cycle 45/55%
- Differential Output Voltage (VoD): 0.247V min, 0.330V typ, 0.454V max.
- Offset Voltage (VOS): 1.125V min, 1.250V typ, 1.375V max.
- Output Voltage Levels:
Output Low (VoL): 0.9V min
Output High (VoH): 1.6V max

Output Control

- Enable/Disable:
Logic '1' ($\geq 70\%$ Vs) to pad 1 enables oscillator output.
Logic '0' ($\leq 30\%$ Vs) to pad 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state.
No connection to pad 1 enables oscillator output.
Standby Current: $10\mu\text{A}$ max

Noise Parameters

- Phase Jitter (12kHz to 20MHz): 1ps rms max

Environmental Parameters

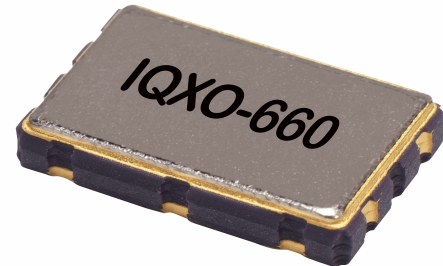
- Storage Temperature Range: -55 to 125°C
- Shock: MIL-STD-202, Method 213, Condition E: 1000G , 0.5ms , half sine wave.
- Vibration: MIL-STD-883, Method 2007, Condition A: 20G

Compliance

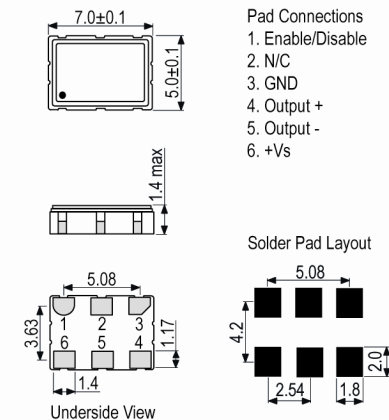
- RoHS Status (2011/65/EU) Compliant
- REACH Status Compliant
- MSL Rating (JDEC-STD-033): Not Applicable

Packaging Details

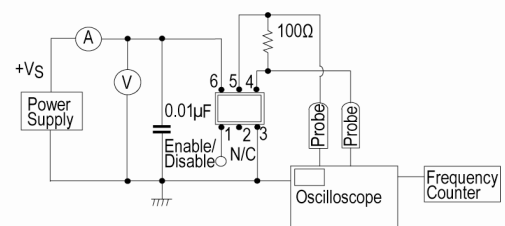
- Pack Style: Reel Tape & reel in accordance with EIA-481-D
Pack Size: 1,000
- Alternative packing option available



Outline (mm)



Test Circuit

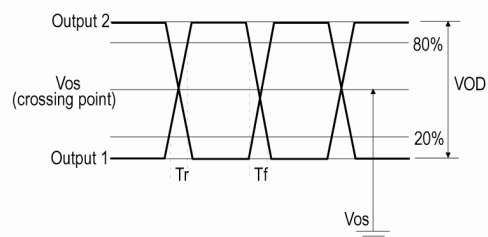


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