

Surge Protected, Single Input, Dual Output Load Switch with OVP

Features

- Single Input, Dual Output Low On- Resistance Switch
 - ▶ VBUS to OUT: typ. 23mΩ
 - ▶ VBUS to SYS: typ. 30mΩ (Reverse Blocking)
- Wide Input Voltage Range: 2.7V – 13.5V
 - ▶ VBUS Abs Max: 28V
- Surge and ESD Protected Input
 - ▶ Surge Protection
 - IEC61000-4-5: > ±100V
 - ▶ ESD Protection
 - IEC61000-4-2 (Level 4) VBUS
 - Contact: ±8kV
 - Air Gap: ±15kV
 - HBM: 2kV All Pins
- Integrated Over-voltage Protection (OVP)
 - ▶ VBUS to OUT: 13.9V ±400mV
 - ▶ VBUS to SYS: 5.25V ±250mV
- Maximum Continuous Current
 - ▶ VBUS to OUT: 3.5A
 - ▶ VBUS to SYS: 6A
- Dual Enable Control with Independent Shutdown Control
 - ▶ Active LOW VBUS to OUT
 - ▶ Active HIGH VBUS to SYS
 - ▶ Active HIGH Shutdown
- VBUS detection LDO
- VBUS to SYS FLAG
- Over Temperature Protection
- Pb-free 28-Bump, WLCSP 2.96mm x 1.67mm
- -40°C to 85°C Operating Temperature Range

Brief Description

The KTS1678 features two low resistance power switches configured as single input, dual output, change-over switch. The input to both switches is protected against VBUS surge voltages of up to ±100V, and is also protected against over-voltage, with preset trip points on both the VBUS to OUT and VBUS to SYS paths, providing protection to downstream components from abnormal input conditions.

The main switch (VBUS to OUT) features a unidirectional active-LOW enabled 3.5A rated MOSFET, with an OVP trip point of 13.9V ±400mV. The secondary switch (VBUS to SYS) is an active-HIGH enabled, reverse-blocking 6.0A rated MOSFET, with an OVP trip point of 5.25V ±250mV. The input to both switches is rated up to a maximum of 28V.

When VBUS is greater than 2.7V, the POK LDO provides an “always ON” power source, regardless of the OVLO, EN1 and EN2 state, to power downstream components permitting operation without an installed battery.

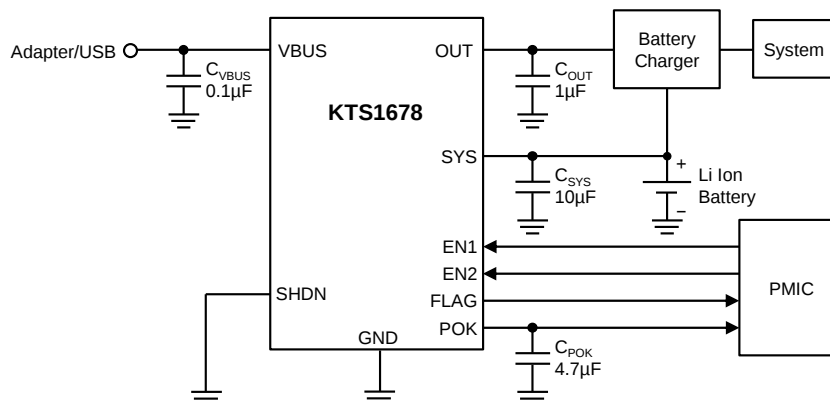
The KTS1678 also features an active-HIGH SHUTDOWN pin to conserve power, plus over-temperature thermal protection.

The KTS1678 is packaged in advanced, fully “green” compliant, 2.96mm x 1.67mm, Wafer-Level Chip-Scale Package (WLCSP).

Applications

- Smartphones and Tablets
- Mobile Internet Devices
- Wearables
- Portable Devices

Typical Application





Ordering Information

Part Number	Marking ¹	Operating Temperature	Package
KTS1678EUQ-TR	JVXXYYZZZZ	-40°C to +85°C	WLCSP28

1. "JVXXYYZZZZ" is the device ID code, date code, assembly code and serial number.

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